



SERVICE MANUAL

VHF MARINE TRANSCEIVER

IC-M603

S-14310XZ-C1
Sep. 2006

Icom Inc.

INTRODUCTION

This service manual describes the latest service information for the **IC-M603** VHF MARINE TRANSCEIVER at the time of publication.

MODEL	VERSION	MICROPHONE
IC-M603	UK	HM-126RB
	EUR	
	HOL	
	FRG	

To upgrade quality, any electrical or mechanical parts and internal circuits are subject to change without notice or obligation.

CAUTION

NEVER connect the transceiver to an AC outlet or to a DC power supply that uses more than 15.6 V. This will ruin the transceiver.

DO NOT reverse the polarities of the power supply when connecting the transceiver.

DO NOT apply an RF signal of more than 20 dBm (100 mW) to the antenna connector. This could damage the transceiver's front end.



ORDERING PARTS

Be sure to include the following four points when ordering replacement parts:

1. 10-digit order numbers
2. Component part number and name
3. Equipment model name and unit name
4. Quantity required

<SAMPLE ORDER>

1110003491 S.IC TA31136FNG IC-M603 MAIN UNIT 5 pieces
8820001210 Screw 2438 screw IC-M603 Top cover 10 pieces
Addresses are provided on the inside back cover for your convenience.

REPAIR NOTES

1. Make sure a problem is internal before disassembling the transceiver.
2. **DO NOT** open the transceiver until the transceiver is disconnected from its power source.
3. **DO NOT** force any of the variable components. Turn them slowly and smoothly.
4. **DO NOT** short any circuits or electronic parts. An insulated tuning tool **MUST** be used for all adjustments.
5. **DO NOT** keep power ON for a long time when the transceiver is defective.
6. **DO NOT** transmit power into a signal generator or a sweep generator.
7. **ALWAYS** connect a 50 dB to 60 dB attenuator between the transceiver and a deviation meter or spectrum analyzer when using such test equipment.
8. **READ** the instructions of test equipment thoroughly before connecting equipment to the transceiver.

TABLE OF CONTENTS

SECTION 1	SPECIFICATIONS	
SECTION 2	INSIDE VIEWS	
2 - 1	IC-M603	2 - 1
2 - 2	HM-157 (OPTIONAL PRODUCT)	2 - 2
SECTION 3	DISASSEMBLY INSTRUCTION	
SECTION 4	CIRCUIT DESCRIPTION	
4 - 1	RECEIVER CIRCUITS	4 - 1
4 - 2	TRANSMITTER CIRCUITS	4 - 3
4 - 3	PLL CIRCUITS	4 - 4
4 - 4	DSC CIRCUITS	4 - 5
4 - 5	LOGIC CIRCUITS	4 - 5
4 - 6	POWER SUPPLY CIRCUITS	4 - 6
4 - 7	PORT ALLOCATIONS	4 - 6
SECTION 5	ADJUSTMENT PROCEDURES	
5 - 1	PREPARATION	5 - 1
5 - 2	PLL ADJUSTMENTS	5 - 2
5 - 3	RECEIVER ADJUSTMENTS	5 - 3
SECTION 6	PARTS LIST	
SECTION 7	MECHANICAL PARTS	
7 - 1	IC-M603	7 - 1
7 - 2	HM-126	7 - 3
7 - 3	HM-157 (OPTIONAL PRODUCT)	7 - 3
SECTION 8	SEMI-CONDUCTOR INFORMATION	
SECTION 9	UNIT LAYOUTS	
9 - 1	VR UNIT	9 - 1
9 - 2	SQL UNIT	9 - 1
9 - 3	DIAL UNIT	9 - 1
9 - 4	CONNECT UNIT	9 - 1
9 - 5	LOGIC UNIT	9 - 2
9 - 6	MAIN UNIT	9 - 4
9 - 7	AF UNIT	9 - 6
9 - 8	HM-126	9 - 8
9 - 9	HM-157 (OPTIONAL PRODUCT)	9 - 8
SECTION 10	BLOCK DIAGRAM	
SECTION 11	WIRING DIAGRAM	
SECTION 12	VOLTAGE DIAGRAM	
12 - 1	LOGIC UNIT	12 - 1
12 - 2	MAIN UNIT	12 - 2
12 - 3	AF UNIT	12 - 4
12 - 4	HM-126	12 - 5
12 - 5	HM-157 (OPTIONAL PRODUCT)	12 - 6

SECTION 1 SPECIFICATIONS

■ GENERAL

- Frequency coverage : 156.000–161.450 MHz (TX)
156.000–163.425 MHz (RX)
- Mode : 16K0G3E (FM) / 16K0G2B (DSC)
- Power supply voltage (negative ground) : 13.8 V DC (10.8–15.6 V)
- Operating temperature range : –20°C to +60°C
- Current drain (at 13.8 V DC) : Transmit at 25 W 5.5 A approx.
Receive max. audio 1.5 A approx.
- Antenna impedance : 50 Ω (nominal)
- Dimensions (projections not included) : 220(W)×110(H)×109.4(D) mm
- Weight : 1400 g

■ TRANSMITTER

- Output power (at 13.8 V DC) : High 25 W
Low 1 W
- Modulation : Variable reactance frequency modulation
- Maximum frequency deviation : ±5 kHz
- Frequency error : ±0.3 kHz
- Spurious emissions : Less than 0.25 μW
- Adjacent channel power : More than 70 dB
- Residual modulation : More than 40 dB
- Audio harmonic distortion : Less than 10% at 60% deviation
- Audio frequency response : +1 dB to –3 dB of 6 dB/octave from 300 Hz to 3000 Hz
- Microphone impedance : 2 kΩ

■ RECEIVER

- Receive system : Double conversion superheterodyne system
- Intermediate frequencies : 1st 21.7 MHz^{*1}, 30.15 MHz^{*2}
2nd 450 kHz
- Sensitivity : Less than –5 dBμ at 12 dB SINAD (for both regular and CH70)
- Squelch sensitivity : Less than –5 dBμ at threshold
- Adjacent channel selectivity : More than 75 dB
- Spurious response : More than 75 dB
- Intermodulation : More than 75 dB
- Hum and noise : More than 45 dB
- Audio output power (at 13.8 V DC) : 2.0 W typical at 10% distortion with a 4 Ω load
- Audio frequency response : +1 dB to –3 dB of –6 dB octave from 300 Hz to 3000 Hz

^{*1}Channel 70, ^{*2}Normal channels

Specifications are measured in accordance with EN301 025-2, -3.

All stated specifications are subject to change without notice or obligation.

■ VHF MARINE CHANNEL LIST

Channel number			Frequency (MHz)	
USA	INT	CAN	Transmit	Receive
	01	01	156.050	160.650
01A			156.050	156.050
	02	02	156.100	160.700
	03	03	156.150	160.750
03A			156.150	156.150
	04		156.200	160.800
		04A	156.200	156.200
	05		156.250	160.850
05A		05A	156.250	156.250
06	06	06	156.300	156.300
	07		156.350	160.950
07A		07A	156.350	156.350
08	08	08	156.400	156.400
09	09	09	156.450	156.450
10	10	10	156.500	156.500
11	11	11	156.550	156.550
12	12	12	156.600	156.600
13* ²	13	13* ¹	156.650	156.650
14	14	14	156.700	156.700
15* ²	15* ¹	15* ¹	156.750	156.750
16	16	16	156.800	156.800
17* ¹	17	17* ¹	156.850	156.850
	18		156.900	161.500
18A		18A	156.900	156.900
	19		156.950	161.550
19A		19A	156.950	156.950
20	20	20* ¹	157.000	161.600
20A			157.000	157.000

Channel number			Frequency (MHz)	
USA	INT	CAN	Transmit	Receive
	21	21	157.050	161.650
21A		21A	157.050	157.050
		21b	Rx only	161.650
	22		157.100	161.700
22A		22A	157.100	157.100
	23	23	157.150	161.750
23A			157.150	157.150
24	24	24	157.200	161.800
25	25	25	157.250	161.850
		25b	Rx only	161.850
26	26	26	157.300	161.900
27	27	27	157.350	161.950
28	28	28	157.400	162.000
		28b	Rx only	162.000
	60	60	156.025	160.625
	61		156.075	160.675
61A		61A	156.075	156.075
	62		156.125	160.725
		62A	156.125	156.125
	63		156.175	160.775
63A			156.175	156.175
	64	64	156.225	160.825
64A		64A	156.225	156.225
	65		156.275	160.875
65A	65A	65A	156.275	156.275
	66		156.325	160.925
66A	66A	66A* ¹	156.325	156.325
67* ²	67	67	156.375	156.375

Channel number			Frequency (MHz)	
USA	INT	CAN	Transmit	Receive
68	68	68	156.425	156.425
69	69	69	156.475	156.475
70* ³	70* ³	70* ³	156.525	156.525
71	71	71	156.575	156.575
72	72	72	156.625	156.625
73	73	73	156.675	156.675
74	74	74	156.725	156.725
75* ¹	75* ¹	75* ¹	156.775	156.775
76* ¹	76* ¹	76* ¹	156.825	156.825
77* ¹	77	77* ¹	156.875	156.875
	78		156.925	161.525
78A		78A	156.925	156.925
	79		156.975	161.575
79A		79A	156.975	156.975
	80		157.025	161.625
80A		80A	157.025	157.025
	81		157.075	161.675
81A		81A	157.075	157.075
	82		157.125	161.725
82A		82A	157.125	157.125
	83	83	157.175	161.775
83A		83A	157.175	157.175
		83b	Rx only	161.775
84	84	84	157.225	161.825
84A			157.225	157.225
85	85	85	157.275	161.875
85A			157.275	157.275
86	86	86	157.325	161.925

Channel number			Frequency (MHz)	
USA	INT	CAN	Transmit	Receive
86A			157.325	157.325
87	87	87	157.375	161.975
87A			157.375	157.375
88	88	88	157.425	162.025
88A			157.425	157.425

WX channel	Frequency (MHz)	
	Transmit	Receive
1	RX only	162.550
2	RX only	162.400
3	RX only	162.475
4	RX only	162.425
5	RX only	162.450
6	RX only	162.500
7	RX only	162.525
8	RX only	161.650
9	RX only	161.775
10	RX only	163.275

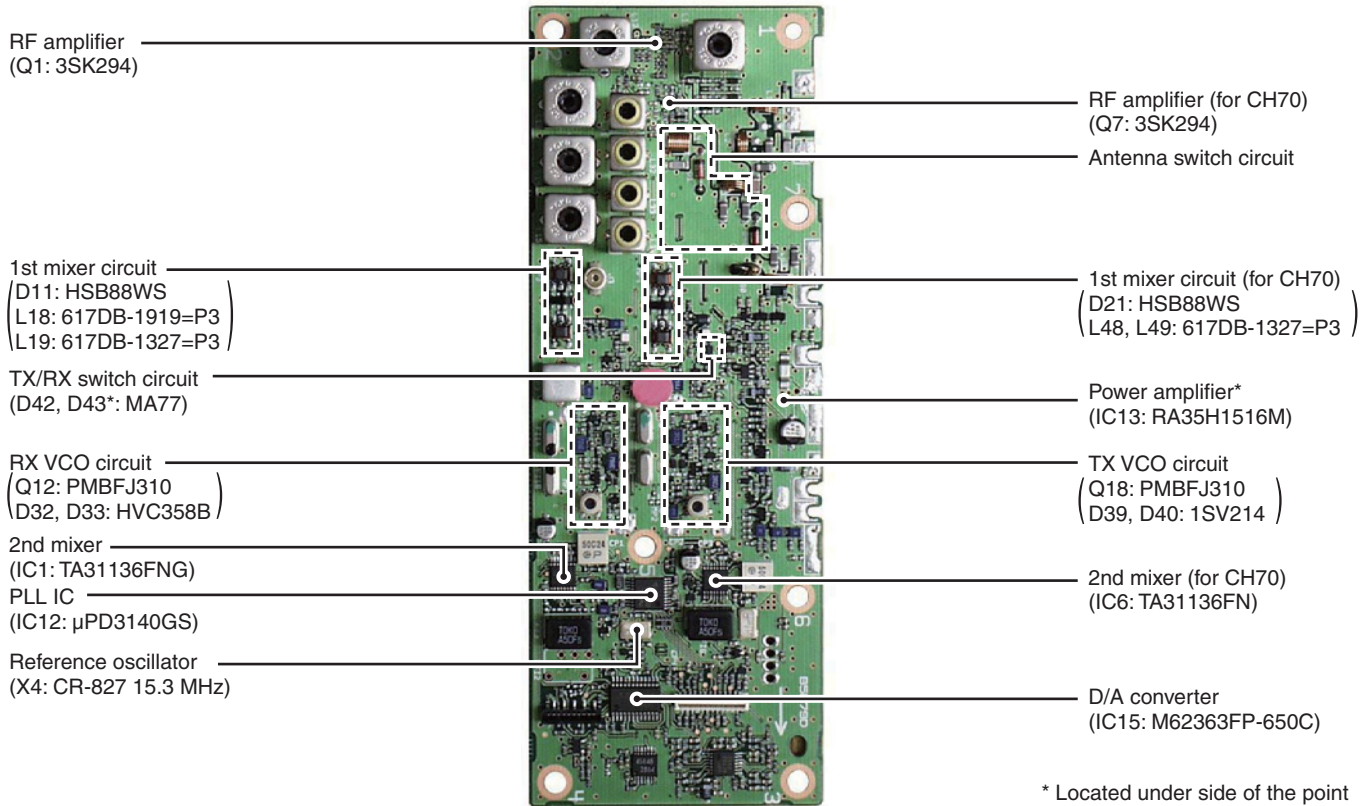
*¹ Low power only. *² Momentary high power. *³ DSC operation only.

NOTE: Simplex channels, 3, 21, 23, 61, 64, 81, 82 and 83 **CANNOT** be lawfully used by the general public in U.S.A. waters.

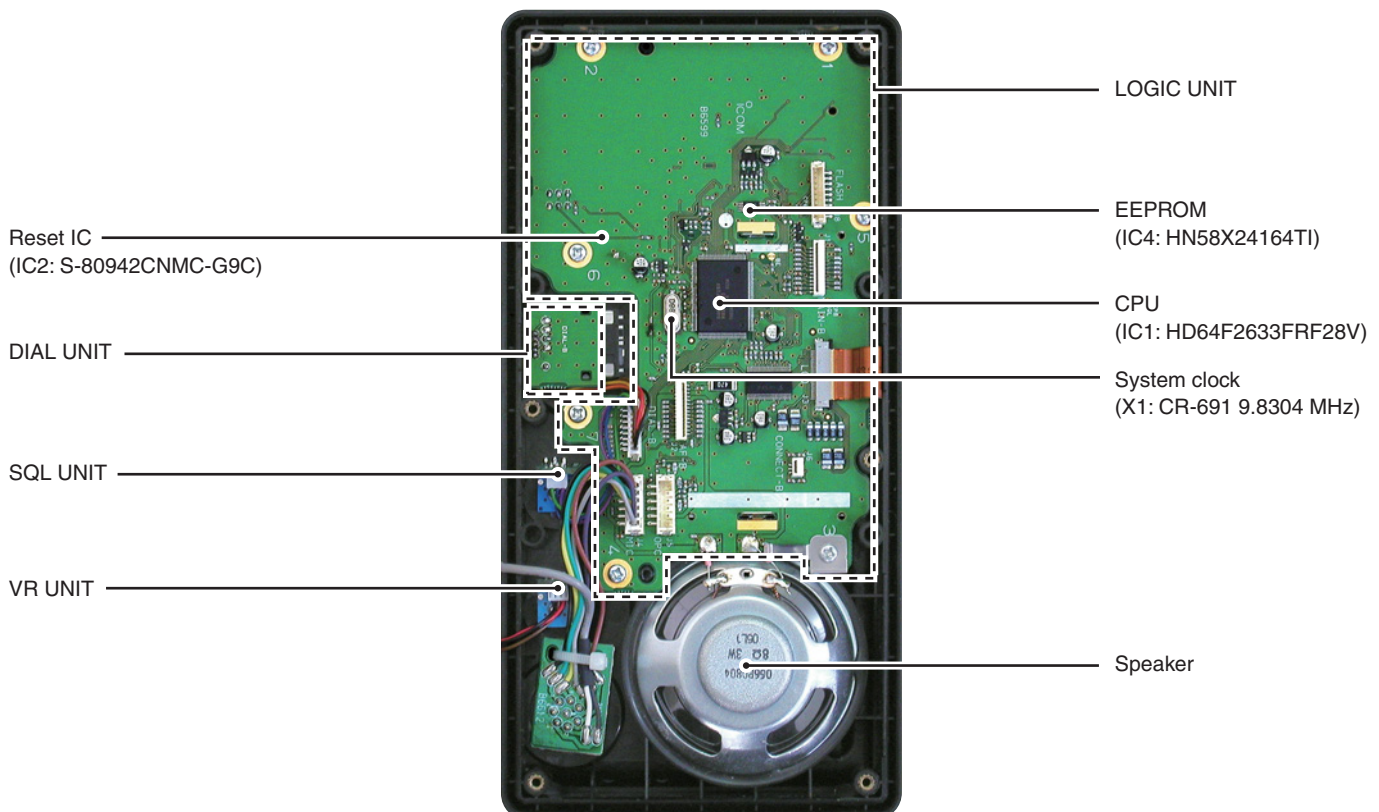
SECTION 2 INSIDE VIEWS

2-1 IC-M603

• MAIN UNIT

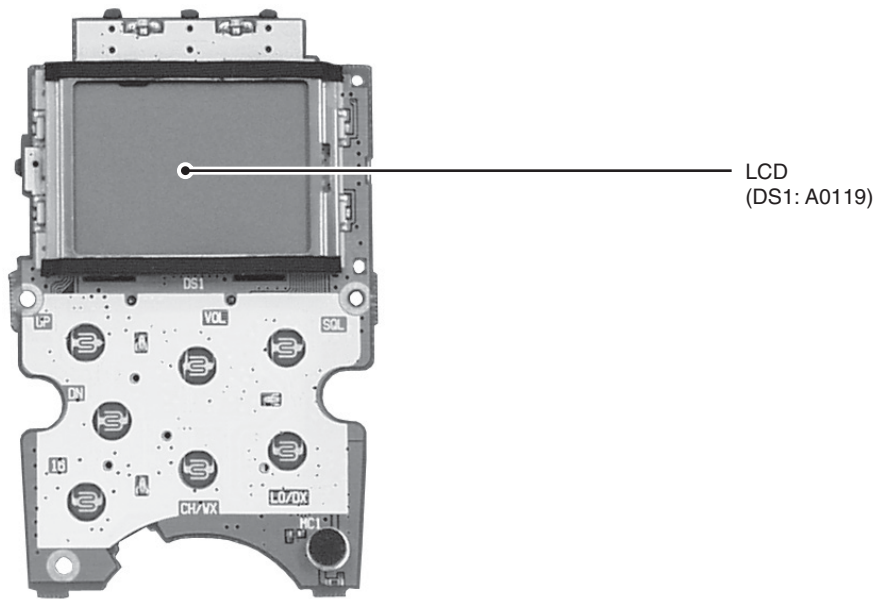


• LOGIC UNIT

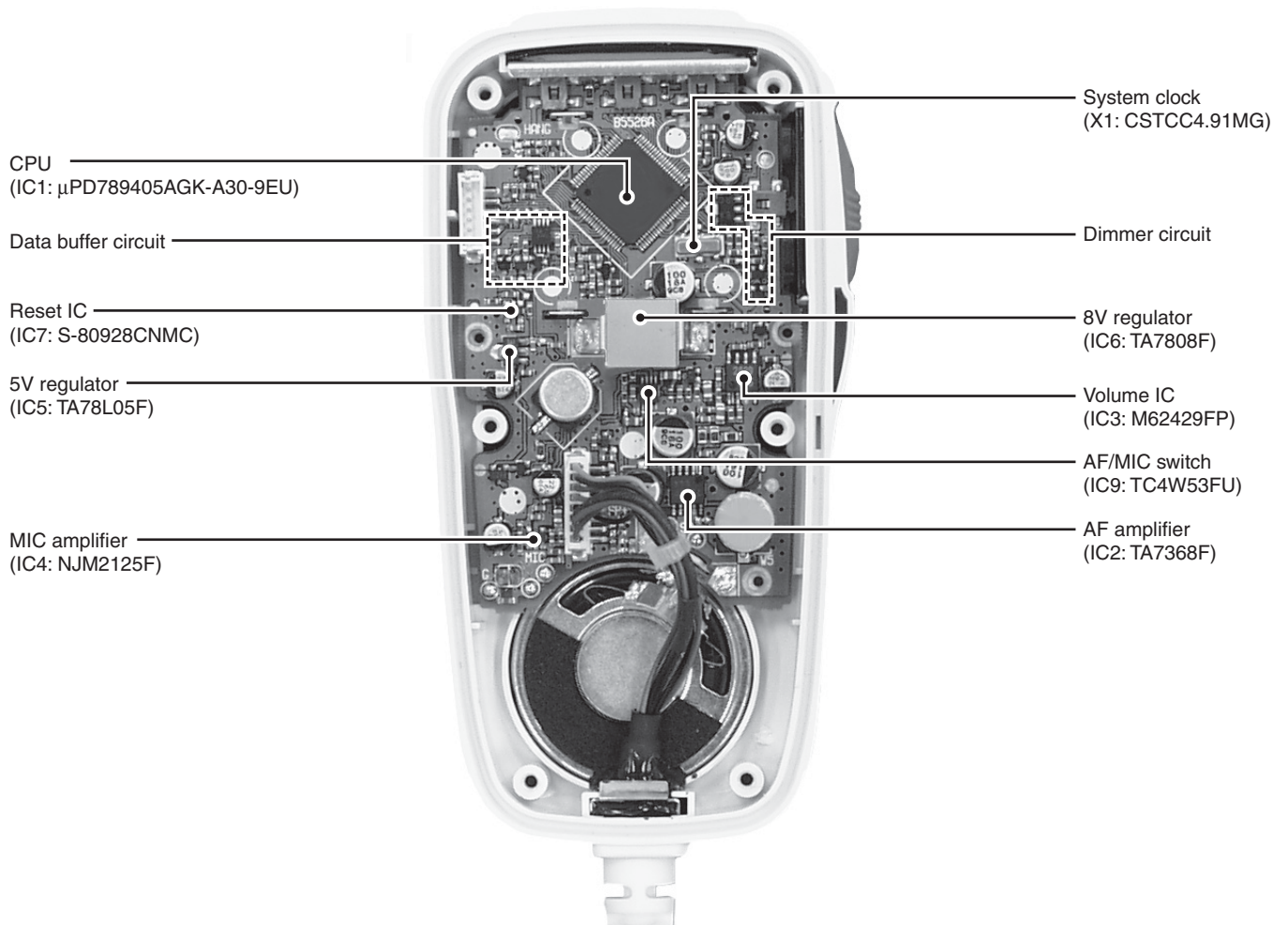


2-2 HM-157 (Optional product)

• TOP VIEW



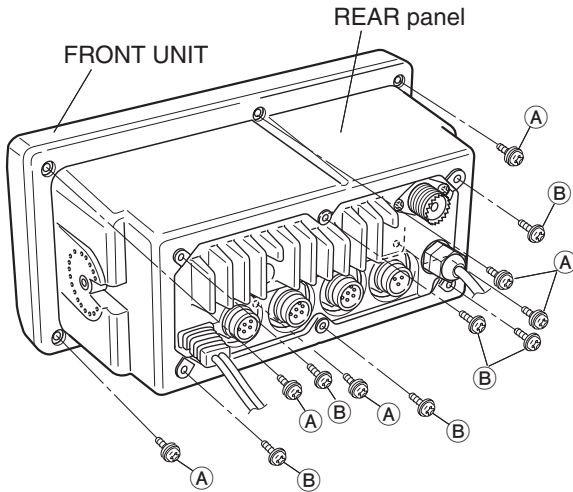
• BOTTOM VIEW



SECTION 3 DISASSEMBLY AND OPTIONS INSTRUCTIONS

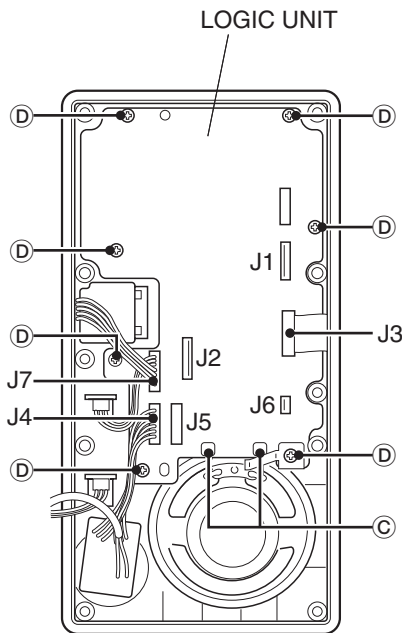
1. Opening the transceiver case

- ① Unscrew 6 screws (A), and remove the front unit.
- ② Unscrew 6 screws (B), and remove the rear panel.



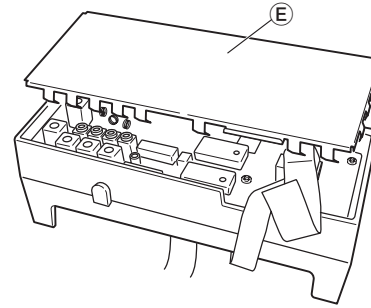
2. Removing the LOGIC UNIT

- ① Disconnect connector from J5 and flat cables from J1, J2 and J6.
- ② Unsolder 2 points (C).
- ③ Disconnect microphone connector from J4 and SQL/DIAL connectors from J7.
- ④ Unscrew 7 screws (D), and remove the LOGIC UNIT.

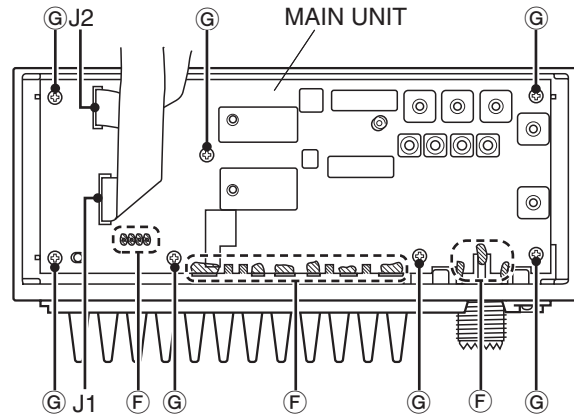


3. Removing the MAIN UNIT

- ① Remove the shield cover (E).



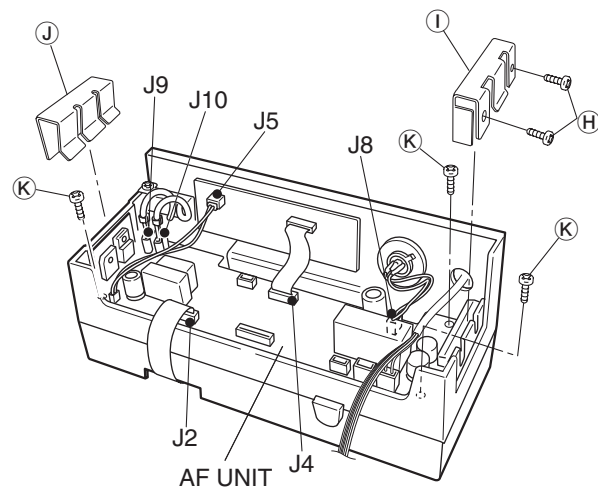
- ② Disconnect flat cables from J1 and J2.
- ③ Unsolder 17 points (F).
- ④ Unscrew 7 screws (G), and remove the MAIN UNIT.



▨ Unsolder point

4. Removing the AF UNIT

- ① Disconnect 4 connectors from J5, J8, J9 and J10.
- ② Disconnect 2 flat cables from J2 and J4.
- ③ Unscrew 2 screws (H), and remove 2 clips (I, J).
- ④ Unscrew 3 screws (K), and remove the AF UNIT.



SECTION 4 CIRCUIT DESCRIPTION

4-1 RECEIVER CIRCUITS

4-1-1 ANTENNA SWITCHING CIRCUIT (MAIN UNIT)

The antenna switching circuit functions as a low-pass filter while receiving and as resonator circuit while transmitting. The circuit does not allow transmit signals to enter the receiver circuits.

Received signals enter the MAIN unit from the antenna connector and pass through the low-pass filter (L1, L2). The signals are then applied to the RF circuit via the antenna switching circuit (D1, L3, L4, C7–C9).

4-1-2 RF CIRCUIT (MAIN UNIT)

The RF circuit amplifies signals within the range of frequency coverage and filters out-of-band signals.

The signals from the antenna switching circuit pass through the tunable bandpass filter (L11, C23–C25) which the object signals are led to each RF amplifier of channel 70 circuit (Q7) or other channels (except channel 70) circuit (Q1).

• CHANNEL 70 CIRCUIT

The amplified signals from the RF amplifier (Q7) are applied to the 4-stage bandpass filter (L31–L34, C407, C408, C410–C423) to suppress unwanted signals and improve the selectivity. The signals are then applied to the 1st mixer circuit for channel 70.

• OTHER CHANNELS CIRCUIT

The amplified signals from the RF amplifier (Q1) are applied to the 4-stage bandpass filter (L12–L15, C33, C34, C36–C48) to suppress unwanted signals and improve the selectivity. The signals are then applied to the 1st mixer circuit for other channels.

4-1-3 1ST MIXER AND 1ST IF CIRCUITS (MAIN UNIT)

The 1st mixer circuit converts the received signal into a fixed frequency of the 1st IF signal with a 1st LO (VCO output) frequency. By changing the 1st LO frequency, only the desired frequency will pass through a pair of crystal filters at the next stage of the mixer.

• CHANNEL 70 CIRCUIT

The signals from the RF circuit are mixed with the 1st LO signals at the 1st mixer circuit (D21, L48, L49) to produce a 21.7 MHz 1st IF signal.

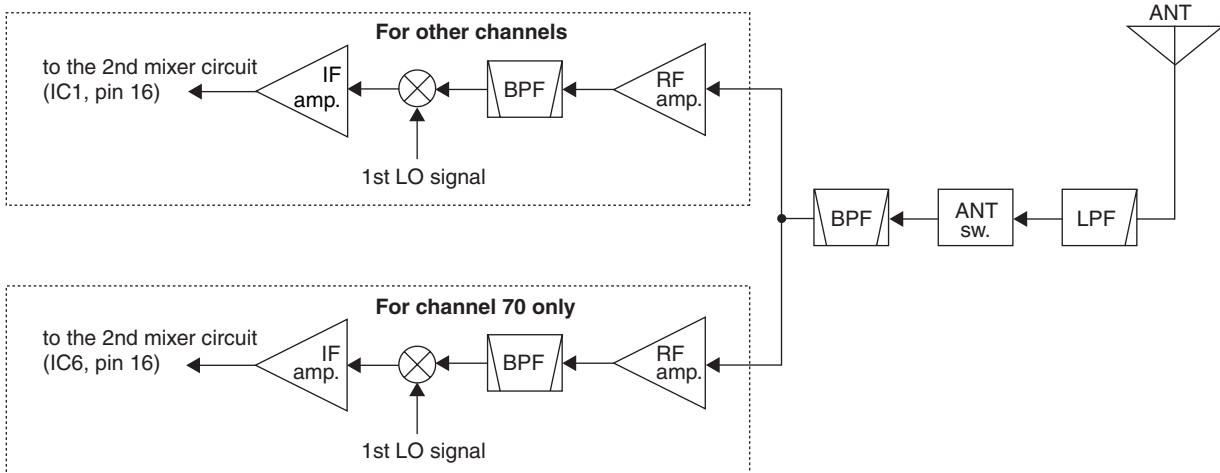
The 1st IF signal is amplified at the 1st IF amplifiers (Q8, Q9), and then passes through the pair of crystal bandpass filters (F14, F15) to suppress out-of-band signals. The filtered signal is then amplified at the 2nd IF amplifier (Q10), and is then applied to the 2nd mixer circuit (IC6).

• OTHER CHANNELS CIRCUIT

The signals from the RF circuit are mixed with the 1st LO signals at the 1st mixer circuit (D11, L18, L19) to produce a 30.15 MHz 1st IF signal.

The 1st IF signal is amplified at the 1st IF amplifiers (Q3, Q4), and then passes through the crystal bandpass filter (F11) to suppress out-of-band signals. The filtered signal is then amplified at the 2nd IF amplifier (Q2), and is then applied to the 2nd mixer circuit (IC1).

• 1ST MIXER AND 1ST IF CIRCUITS



4-1-4 2ND IF AND DEMODULATOR CIRCUITS (MAIN UNIT)

The 2nd mixer circuit converts the 1st IF signal into a 2nd IF signal. A double superheterodyne system (which converts receive signals twice) improves the image rejection ratio and obtains stable receiver gain.

The FM IF IC (IC6 for channel 70, IC1 for other channels) contains the 2nd local oscillator, 2nd mixer, limiter amplifier, quadrature detector, and noise detector circuits, etc.

• CHANNEL 70 CIRCUIT

The 1st IF signal from the 2nd IF amplifier (Q10) is applied to the 2nd mixer section of FM IF IC (IC6, pin 16), and is mixed with a 21.25 MHz 2nd LO signal, which is generated at the 2nd oscillator section in IC6 and X3, to produce a 450 kHz 2nd IF signal.

The 2nd IF signal from IC6 (pin 3) is passed through the ceramic filter (F16), which unwanted signals are suppressed, and is then applied to the 2nd IF (limiter) amplifier in IC6 (pin 5). The signal is applied to the FM detector section in IC6 for demodulating into AF signals.

The FM detector circuit employs a quadrature detection method (linear phase detection), which uses a ceramic discriminator (X2) for phase delay to obtain a non-adjusting circuit. The detected signal from IC6 (pin 9) is applied to the AF circuit.

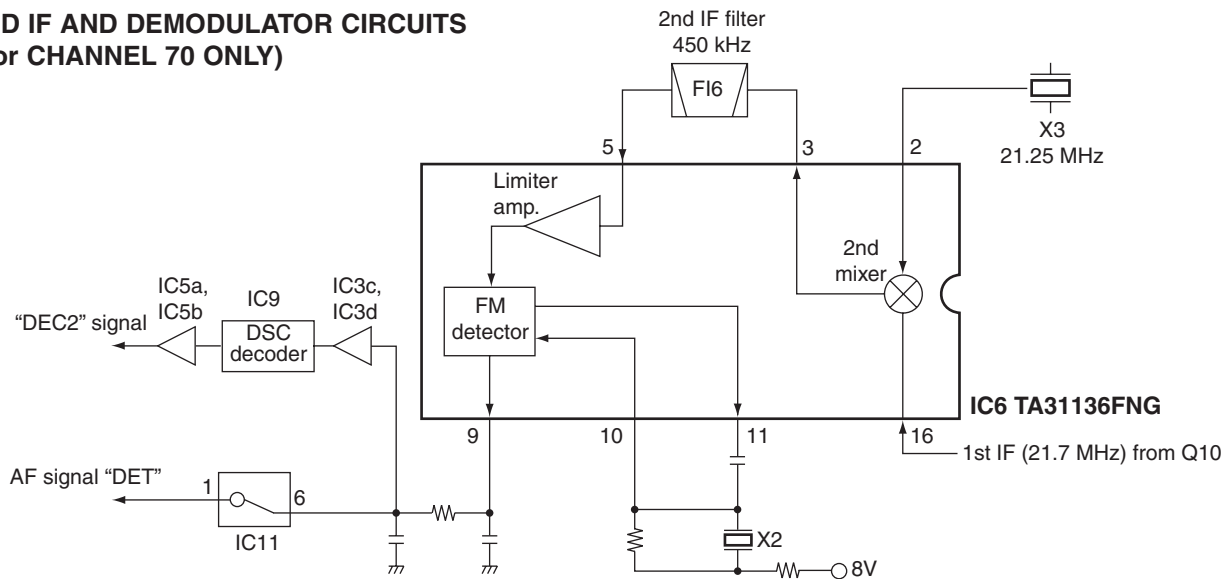
• OTHER CHANNELS CIRCUIT

The 1st IF signal from the 2nd IF amplifier (Q2) is applied to the 2nd mixer section of FM IF IC (IC1, pin 16), and is mixed with a 30.6 MHz 2nd LO signal, which is generated at the PLL circuit using the reference frequency (15.3 MHz), to produce a 450 kHz 2nd IF signal.

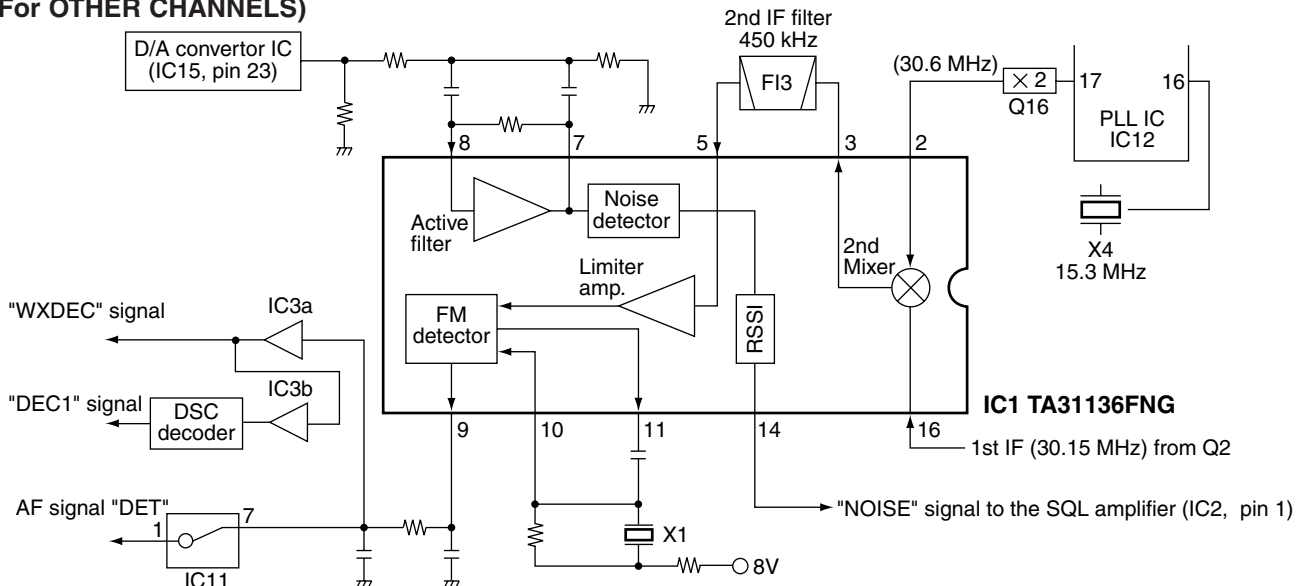
The 2nd IF signal from IC1 (pin 3) is passed through the ceramic filter (F13), which unwanted signals are suppressed, and is then applied to the 2nd IF (limiter) amplifier in IC1 (pin 5). The signal is applied to the FM detector section in IC1 for demodulating into AF signals.

The FM detector circuit employs a quadrature detection method (linear phase detection), which uses a ceramic discriminator (X1) for phase delay to obtain a non-adjusting circuit. The detected signal from IC1 (pin 9) is applied to the AF circuit.

• 2ND IF AND DEMODULATOR CIRCUITS (For CHANNEL 70 ONLY)



• 2ND IF AND DEMODULATOR CIRCUITS (For OTHER CHANNELS)



4-1-5 AF AMPLIFIER CIRCUIT (AF UNIT)

The AF amplifier circuit amplifies the demodulated signals to drive a speaker. The AF circuit includes an AF mute circuit for the squelch.

AF signals from the FM IF ICs (channel 70; IC6, pin 9, other channels IC1, pin 9) are passed through the analog switch (IC7, pins 10, 11) via the “DET” signal, and are applied to the de-emphasis circuit (R31, C41). The de-emphasis circuit is an integrated circuit with frequency characteristic of -6 dB/octave.

The signals pass through the bandpass filter (Q11, Q12), and are then applied to the AF mute switch (Q13). The signals passed through the [VOLUME] control (VR unit; R1), and are then applied to the AF power amplifier (IC3, pin 1) to obtain 5 W AF audio output power. The amplified AF signals drive the internal speaker as “SP+” signal directly or external speaker as “AF” signal via the RL2.

4-1-6 SQUELCH CIRCUIT (MAIN AND LOGIC UNITS)

A squelch circuit cuts out AF signals when no RF signals are received. By detecting noise components in the AF signals, the squelch circuit switches the AF mute switch.

A portion of the AF signals from the FM IF IC (IC1, pin 9) is passed through C89, and is applied to the D/A converter (IC15, pin 24) to control the amplitude. The signal is applied to the FM IF IC’s active filter section (IC1, pin 8). The active filter section amplifies and filters noise components. The filtered signals are applied to the noise detector section and output from pin 14 as the “SQL” signal. The “SQL” signal is amplified at the DC amplifier (IC2) and applied to the main CPU (LOGIC unit; IC1, pin 104) as the “SQL” signal. The main CPU compares “SQL” voltage with “SQLV” voltage from the SQL board, and outputs the “MICM” and “RMUTE” signals to toggle the AF mute switches (Q7, Q13).

4-2 TRANSMITTER CIRCUITS

4-2-1 MICROPHONE AMPLIFIER CIRCUIT (AF UNIT)

The microphone amplifier circuit amplifies audio signals with $+6$ dB/octave pre-emphasis from the microphone to a level needed at the modulation circuit.

• USING HM-126RB/RG

The AF signals from the microphone (ACC unit; HM-126RB/RG) are amplified at the microphone amplifier (IC6a) via the analog switch (IC4, pins 11, 10) as “MIC” signal. A capacitor (C77) and resistor (R73) are connected to the microphone amplifier to obtain the pre-emphasis characteristics.

• USING HM-162

The AF signals from the microphone (ACC unit; HM-162) are amplified at the microphone amplifier (IC6a) via the analog switch (connecting option1 jack: IC16, pins 2, 3, 4; connecting option2 jack: IC16, pins 4, 9, 10) as “AF/MIC1” or “AF/MIC2” signals. A capacitor (C77) and resistor (R73) are connected to the microphone amplifier to obtain the pre-emphasis characteristics.

The amplified signals are applied to the IDC amplifier (IC8a, pin 2) via the analog switch (IC7, pins 2, 3, 9), and are then passed through the splatter filter (IC8b) to suppress unwanted 3 kHz or higher signals. The filtered signals are applied to the modulation circuit.

4-2-2 MODULATION CIRCUIT (MAIN UNIT)

The modulation circuit modulates the VCO oscillating signal (RF signal) using the microphone audio signals.

The audio signals from the splatter filter (IC8b) are passed through the D/A converter IC (IC15, pins 11, 12), and are then applied to the modulation circuit. The applied signals change the reactance of the varactor diode (D37), and modulate the oscillated signal at the TX-VCO (Q18).

4-2-3 PRE-DRIVE AND YGR AMPLIFIERS CIRCUIT (MAIN UNIT)

The drive amplifier circuit amplifies the VCO oscillating signal to a level needed at the power amplifier.

The output signal from VCO circuit is amplified at the buffer amplifiers (Q19 and Q27), and is applied to the TX/RX switch (D43). The transmit signal from the TX/RX switch is amplified at the pre-drive (Q28) and YGR (Q30) amplifiers to obtain an approximate 50 mW signal level. The amplified signal is then applied to the RF power amplifier (IC13).

4-2-4 POWER AMPLIFIER CIRCUIT (MAIN UNIT)

The power amplifier circuit amplifies the driver signal to an output power level.

IC13 is a power module which has amplification output capabilities of about 35 W with 50 mW input. The output signal from IC13 (pin 1) is passed through the antenna switching circuit (D46) and is then applied to the antenna connector via the low-pass filter (L1, L2, L89, C361, C364).

4-2-5 APC CIRCUIT (MAIN UNIT)

The APC (Automatic Power Controller) circuit stabilizes the TX output power.

The RF output signal from the power amplifier (IC13) is detected at the power detector circuit (D47, D48) and is applied to APC controller. The applied voltage compares to "DAPCON" signal from the D/A converter IC (IC15, pin 14), and then outputs the differential bias voltage for power amplifier (IC13, pin 3). Thus the APC circuit maintains a constant output power.

4-3 PLL CIRCUITS

4-3-1 GENERAL

The PLL circuit provides stable oscillation of the transmit frequency and receive 1st LO frequency. The PLL circuit compares the phase of divided VCO frequency with the reference frequency. The PLL output frequency is controlled by the crystal oscillator and divided ratio of the programmable divider.

IC12 is a dual PLL IC, which controls both TX and RX VCO circuits, and contains a prescaler, programmable counter, programmable divider, phase detector, charge pump and etc.

The PLL circuit, using a one chip PLL IC (IC12), directly generates the transmit frequency and receive 1st IF frequency with VCOs. The PLL IC sets the divided ratio based on serial data from the main CPU, and compares the phases of VCO signals with the reference oscillator frequency. The PLL IC detects the out-of-step phase and outputs from pins 8 and 13 for TX and RX, respectively. The reference frequency (15.3 MHz) is oscillated at the reference oscillator (X4).

4-3-2 TX AND CHANNEL 70 (RX) LOOPS

The generated signal at the TX-VCO/CHANNEL 70-VCO (Q18, D37, D39, D40) enters the PLL IC (IC12, pin 2) and is divided at the programmable divider section and is then applied to the phase detector section.

The phase detector compares the input signal with a reference frequency, and then outputs the out-of-phase signal (pulse-type signal) from pin 8.

The pulse-type signal is converted into DC voltage (lock voltage) at the loop filter (R217–R219, C247, C249, C278), and is then applied to the varactor diodes (D39, D40) of the TX-VCO to stabilize the oscillated frequency.

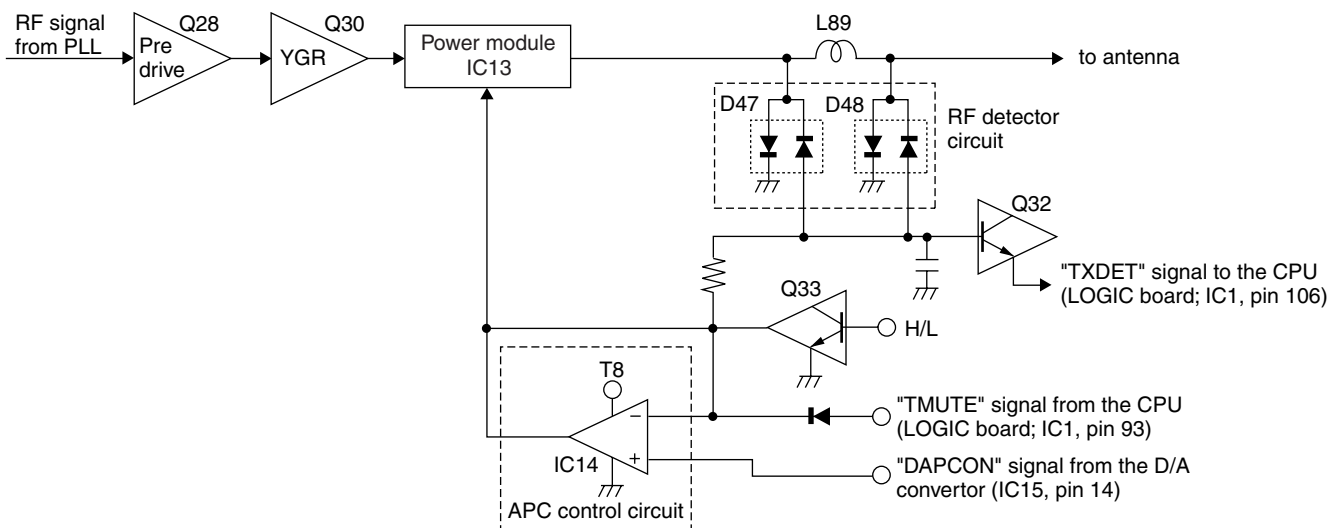
4-3-3 OTHER CHANNELS (RX) LOOP

The generated signal at the RX-VCO (Q12, D32, D33) enters the PLL IC (IC2, pin 19) and is divided at the programmable divider section and is then applied to the phase detector section.

The phase detector compares the input signal with a reference frequency, and then outputs the out-of-phase signal (pulse-type signal) from pin 13.

The pulse-type signal is converted into DC voltage (lock voltage) at the loop filter (R182, R213, R214, C225, C248), and is then applied to the varactor diodes (D32, D33) of the RX-VCO to stabilize the oscillated frequency. The lock voltage from the loop filter is amplified at the buffer amplifier (Q23), and is then applied to the RF circuit.

• APC CIRCUIT



4-3-4 VCO CIRCUIT (MAIN UNIT)

• TX-VCO/CHANNEL 70-VCO (RX) CIRCUITS

The VCO outputs from TX-VCO/CHANNEL 70-VCO (Q18) are amplified at the buffer amplifiers (Q19 and Q27), and are applied to the TX/RX switch circuit (D42, D43). The receiver LO signal is applied to the 1st mixer circuit for CHANNEL 70 (D21, L48, L49) passing through a low-pass filter (L51, L52, C150–C152), and the transmitter signal is applied to the pre-drive amplifier (Q28). A portion of the VCO output signal is re-applied to the PLL IC (IC12, pin 2) via the buffer amplifier (Q15).

• OTHER CHANNELS-VCO (RX) CIRCUITS

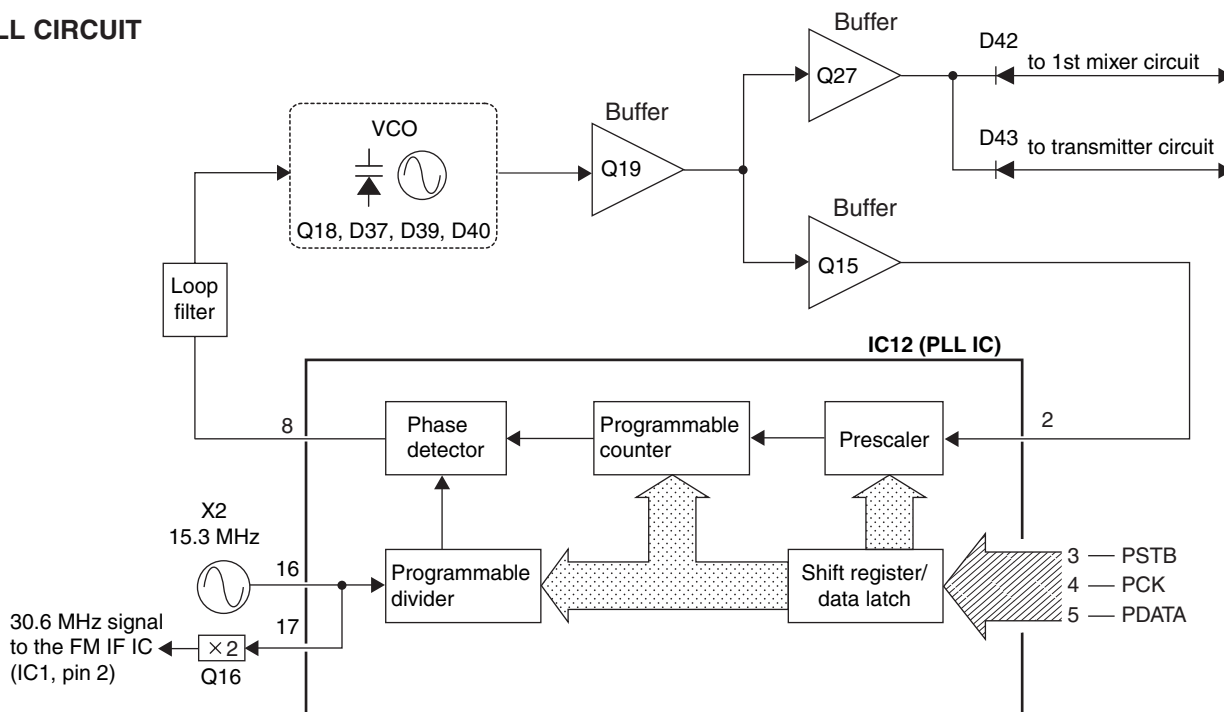
The VCO outputs from OTHER CHANNELS-VCO (Q12) are amplified at the buffer amplifiers (Q13 and Q23). The receiver LO signal is applied to the 1st mixer circuit for OTHER CHANNELS (D11, L18, L19) passing through a low-pass filter (L21, L22, C52–C54). A portion of the VCO output signal is re-applied to the PLL IC (IC12, pin 2 or pin 19) via the buffer amplifier (Q19).

4-4 DSC CIRCUITS

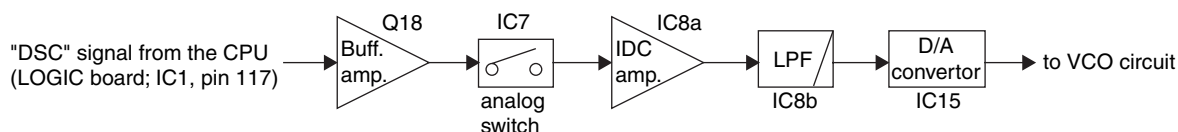
4-4-1 DSC MODULATION CIRCUIT (LOGIC, AF AND MAIN UNITS)

The ATIS signal from the CPU (LOGIC unit; IC1, pin 117) is applied to the buffer amplifier (AF unit; Q18) as “DSC” signal. The signal passes through the analog switch (AF unit; IC7, pin 1), and then applied to IDC amplifier (AF unit; IC8a). Then, the amplified signal is applied to the transmitter circuit.

• PLL CIRCUIT



• DSC CIRCUIT



The signals passed through the splatter filter (AF unit; IC8b) to suppress unwanted 3 kHz or higher signals. The filtered signals are then applied to the TX modulation circuit via the D/A converter IC (MAIN unit; IC15, pins 11, 12) as a DSC modulation signal “MOD”.

4-5 LOGIC CIRCUITS

4-5-1 LOGIC UNIT

• CPU

IC1 is a 8 bit single chip micro-computer, which contains LCD driver, serial I/O, timer, A/D converter, programmable I/O, ROM and RAM.

• SYSTEM CLOCK CIRCUIT

X1 is a crystal oscillator, which oscillates 9.8304 MHz system clock for the main CPU (IC1).

• RESET CIRCUIT

IC2 is a reset IC, which outputs a reset signal (“LOW” pulse) to main CPU (IC1, pin 79) when turning transceiver power ON.

4-6 POWER SUPPLY CIRCUITS

4-6-1 VOLTAGE LINE (MAIN UNIT)

LINE	DESCRIPTION
HV	The 13.8 V from the connected DC power supply.
VCC	Same voltage as the 13.8 V line, and is applied to the AF power amplifiers (AF unit; IC3, IC10), LOGIC unit, etc.
8V	Common 8 V converted from the VCC line at the +8V regulator circuit (AF unit; IC1). The output voltage is applied to the T8 controller (MAIN unit; Q36, Q36), +5 regulator (AF unit; IC2), R8 regulator (AF unit; Q1, Q2), etc.
5V	Common 5 V converted from the 8V line at the +5 regulator circuit (AF unit; IC2). The output voltage is applied to the buffer amplifiers (AF unit; IC19, Q14), expander ICs (AF unit; IC17, IC18), etc.
T8	Transmit 8 V controlled by the T8 control circuit (MAIN unit; Q35, Q36) using the "SEND" signal from main CPU. The output voltage is applied to the pre-drive (MAIN unit; Q28), YGR amplifier (MAIN unit; Q30), APC controller (MAIN unit; IC14), etc.
R8	Receive 8 V controlled by the R8 control circuit (AF unit; Q1, Q2) using the RCV signal from main CPU. The controlled voltage is applied to the bandpass filter (AF unit; Q11, Q12), buffer and IF amplifiers (AF unit; Q2 and Q23), etc.

4-7 PORT ALLOCATIONS

4-7-1 EXPANDER IC (AF unit; IC18)

Pin number	Port name	Description
4	MICS2	Outputs HM-162/2 control signal. High : While transmitting via the HM-162/2.
5	MICS1	Outputs HM-162/1 control signal. High : While transmitting via the HM-162/1.
6	SPS2	Outputs HM-162/2 control signal. High : While receiving via the HM-162/2.
7	SPS1	Outputs HM-162/1 control signal. High : While receiving via the HM-162/1.
11	SP	Outputs the internal speaker (FRONT unit; SP1) control signal. High : The speaker is activating.
12	BPLVL	Outputs beep audio level control signal. Low : Beep audio level is maximum.
13	RCV	Outputs the R8 regulator (AF unit; Q1, Q2) control signal. High : While receiving.
14	HLC	Outputs the Hailer speaker TX/RX select signal. High : While transmitting via the Hailer speaker.

4-7-2 EXPANDER IC (AF unit; IC17)

Pin number	Port name	Description
4	STRU	Outputs scrambler unit bypass control signal. High : Bypassing the scrambler unit.
5	AFSUB	Outputs sound signals to the HM-162. High : Sounding from HM-162.
6	INCMH	Outputs voice signals from IC-M602 to HM-162 using intercom function. High : While receiving.
7	INCHM	Outputs voice signals from HM-162 to IC-M602 using intercom function. High : While transmitting.
11	MIC/DSC	Outputs MIC/DSC modulation circuit control signal. High : While the DSC signal is modulated.
12	HAILIN	Outputs the microphone select signal. High : While using the hailer speaker.
13	FOGC	Outputs fog horn control signal. High : Fog horn is ON.
14	HAILOUT	Outputs the microphone select signal. High : While using the HM-126.

4-7-3 CPU (LOGIC BOARD; IC1)

Pin number	Port name	Description															
40	UNLK	Input port for PLL unlock signal from the PLL IC (MAIN unit; IC12, pin 7). High : While PLL is unlocked.															
41	EDATA	I/O port for the data signals to the EEPROM (IC4, pin 5).															
28	ECK	Outputs a clock signal to the EEPROM (IC4, pin 6).															
34	DEC3	Input port for the decode signal for channel 70 receiver.															
38	DEC1	Input port for the ATIS/DSC decode signals.															
110	BEEP	Outputs beep audio signals.															
58	DATAMC	I/O port for the cloning data from the transceiver.															
59	DATA CM	I/O port for the cloning data to the transceiver.															
63	DATAMH1	I/O port for the communicating signal from the transceiver to the microphone (HM-162/1).															
64	DATAH1M	I/O port for the communicating signal from the microphone (HM-162/1) to the transceiver.															
66	DATANM	I/O port for the GGA signals															
69	DATAMN	I/O port for the NMEA data.															
70	PDATA	Outputs a data signal to the PLL IC (MAIN unit; IC12, pin 5).															
71	PCK	Outputs a clock signal to the PLL IC (MAIN unit; IC12, pin 4).															
6	OPTIN	Outputs the voice scrambler unit (UT-112) detecting signal. Low : While UT-112 is connecting.															
92	RMUTE	Outputs RX muting signal. High : While RX signal is muting.															
93	TMUTE	Outputs transmit mute signal. High : While TX muting.															
94	SEND	Outputs T8 regulator control signal. High: While transmitting.															
96	H/L	Output port for RF output power (High or Low) select signal. Low : While Low power is selected.															
97 98	ATT2C ATT1C	Output RX attenuator control signals. <table border="1" style="margin-left: 20px;"> <thead> <tr> <th>ATT level</th> <th>OFF</th> <th>ON (1)</th> <th>ON (2)</th> <th>MAX.</th> </tr> </thead> <tbody> <tr> <td>ATT1C</td> <td>1</td> <td>1</td> <td>0</td> <td>1</td> </tr> <tr> <td>ATT2C</td> <td>1</td> <td>0</td> <td>1</td> <td>1</td> </tr> </tbody> </table>	ATT level	OFF	ON (1)	ON (2)	MAX.	ATT1C	1	1	0	1	ATT2C	1	0	1	1
ATT level	OFF	ON (1)	ON (2)	MAX.													
ATT1C	1	1	0	1													
ATT2C	1	0	1	1													
103	WXDEC	Input port for the weather alert signal.															
104	SQL	Input port for the FM IF IC (MAIN unit; IC1, pin 14)'s noise amplifier detecting signal.															

Pin number	Port name	Description
105	LBAT	Input port for the low-battery detecting signal. Low battery indicator appears when the battery becomes less than 2.58 V
106	TXDET	Input port for transmit detecting signal.
107	TEMP	Input port for the inside temperature detecting signal.
111-114	DIAL1-DIAL4	Input ports for the dial data signals.
115	PTT	Input port for the HM-136's PTT button detecting signal. Low : While PTT button is pushed.
26	HANG	Input port for the microphone hanger detecting signal Low : The microphone on hook.
117	DSC	Outputs ATIS/DSC encode signals.
120	SCON	Outputs the voice scrambler unit (UT-112) control signal.
121	OPSTB	Outputs a strobe signal to the voice scrambler unit (UT-112).
124	DASTB	Outputs a strobe signal to the PLL IC (MAIN unit; IC12, pin 3).
125	PSTB	Outputs a strobe signal to the D/A convertor IC (MAIN unit; IC15, pin 6).
126	DATAH2M	I/O port for the communicating signal from the microphone (HM-162/2) to the transceiver.
127	DATAMH2	I/O port for the communicating signal from the transceiver to the microphone (HM-162/2).

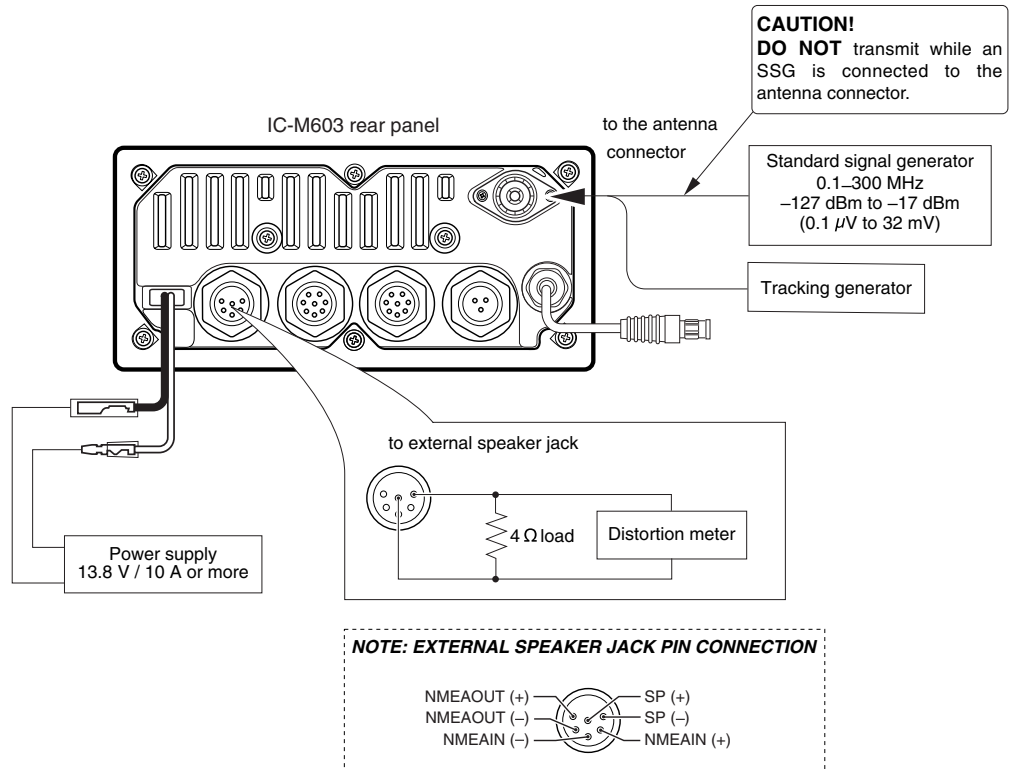
SECTION 5 ADJUSTMENT PROCEDURES

5-1 PREPARATION

REQUIRED TEST EQUIPMENT

EQUIPMENT	GRADE AND RANGE	EQUIPMENT	GRADE AND RANGE
DC power supply	Output voltage : 13.8 V DC Current capacity : 10 A or more	Standard signal generator (SSG)	Frequency range : 0.1–300 MHz Output level : 0.1 μ V to 32 mV (–127 to –17 dBm)
External speaker	Input impedance : 4 Ω Capacity : 5 W or more	DC voltmeter	Input impedance : 50 k Ω /V DC or better
Tracking generator	Frequency range : 100–300 MHz Output level : 0.1 μ V to 32 mV (–127 dBm to –17 dBm)	Distortion meter	Frequency range : 1 kHz \pm 10 % Measuring range : 1–100 %

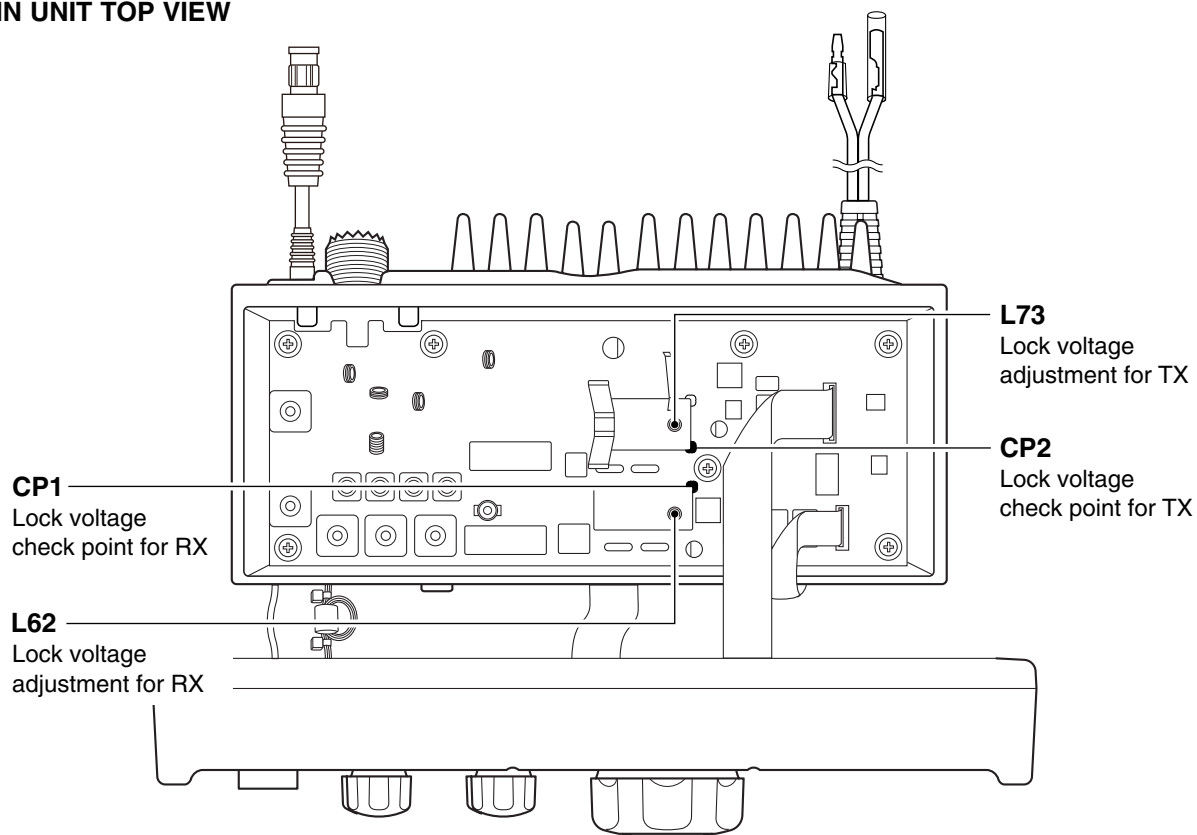
CONNECTION



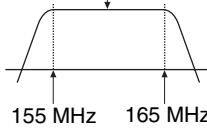
5-2 PLL ADJUSTMENTS

ADJUSTMENT	ADJUSTMENT CONDITION	MEASUREMENT		VALUE	ADJUSTMENT POINT			
		UNIT	LOCATION		UNIT	ADJUST		
LOCK VOLTAGE	1 • Operating channel : ch P2 • Receiving	MAIN	Connect a digital multi-meter or oscilloscope to check point CP1.	3.8 V – 4.0 V	MAIN	L62		
	2 • Operating channel : ch P2 • Output power : Low • Transmitting					Connect a digital multi-meter or oscilloscope to check point CP2.	3.1 V – 3.3 V	L73
	3 • Operating channel : ch 70 • Receiving						2.4–3.7 V	Verify

• MAIN UNIT TOP VIEW

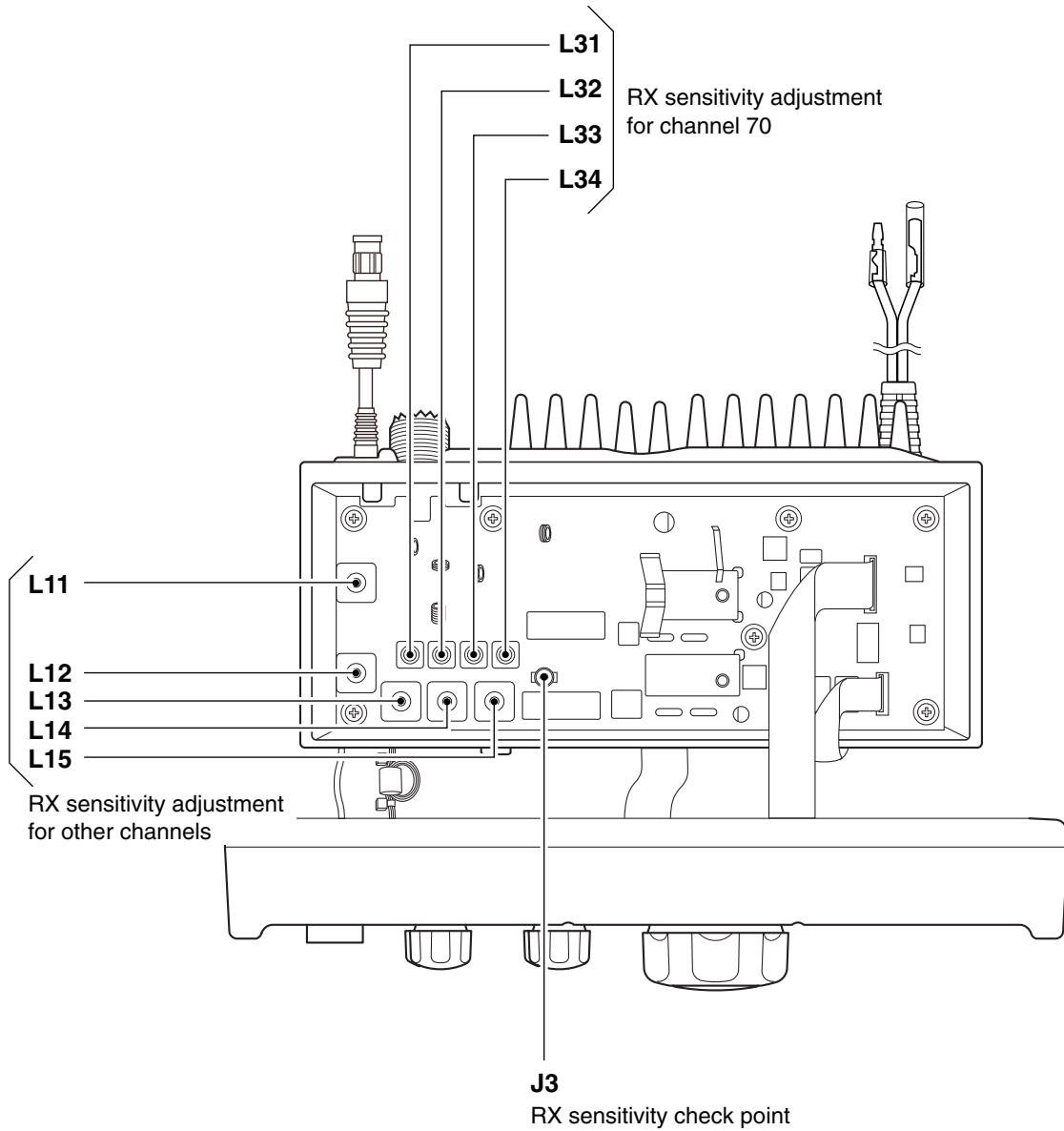


5-3 RECEIVER ADJUSTMENTS

ADJUSTMENT	ADJUSTMENT CONDITION	MEASUREMENT		VALUE	ADJUSTMENT POINT	
		UNIT	LOCATION		UNIT	ADJUST
SENSITIVITY (Except channel 70)	1 <ul style="list-style-type: none"> • Operating channel : ch 16 • Connect a tracking generator's output to the antenna connector and set as: Level : 7.1 mV* (-30 dBm) 	MAIN	Connect a tracking generator's input to the MAIN unit; J3.	Set the flat wave form as shown below. Set to flat wave form  155 MHz 165 MHz	MAIN	L11 L12 L13 L14 L15
(Channel 70)	2 <ul style="list-style-type: none"> • Operating channel : ch 70 • Connect an SSG to the antenna connector and set as: Frequency : 156.525 MHz Level : 10 μV* (-97 dBm) Modulation : 1 kHz Deviation : ±3.5 kHz • Set the internal speaker OFF in the SET mode, and connect a distortion meter with a 4 Ω load to [EXT SP] receptacle. • Receiving 	MAIN	Connect a DC voltmeter to check point CP3.	Maximum voltage	MAIN	L31 L32 L33 L34

*This output level of a standard signal generator (SSG) is indicated as SSG's open circuit.

• MAIN UNIT TOP VIEW



[MAIN UNIT]

Table with columns: REF NO., ORDER NO., DESCRIPTION, M., H/V LOCATION. Contains rows L19 through R77.

[MAIN UNIT]

Table with columns: REF NO., ORDER NO., DESCRIPTION, M., H/V LOCATION. Contains rows R78 through R232.

M.=Mounted side (T: Mounted on the Top side, B: Mounted on the Bottom side)
S.=Surface mount

[MAIN UNIT]

REF NO.	ORDER NO.	DESCRIPTION	M.	H/V LOCATION
C404	4030011600	S.CER C1608 JB 1E 104K-T	T	21.3/25.2
C405	4030006850	S.CER C1608 JB 1H 471K-T	T	18/29.8
C406	4030011600	S.CER C1608 JB 1E 104K-T	T	25.5/24.7
C407	4030009920	S.CER C1608 CH 1H 050B-T	B	21.3/19.2
C408	4030011770	S.CER C1608 CH 1H 060B-T	B	21.3/17.7
C409	4030006860	S.CER C1608 JB 1H 102K-T	B	19.4/25
C410	4030009540	S.CER C1608 CH 1H 1R5B-T	B	24.2/19.7
C411	4030009510	S.CER C1608 CH 1H 010B-T	B	24.9/21.7
C412	4030006990	S.CER C1608 CH 1H 080D-T	B	27.3/19.3
C413	4030009350	S.CER C1608 CH 1H 3R5B-T	B	28.9/19.3
C414	4030009560	S.CER C1608 CH 1H R75B-T	B	31.3/20.1
C415	4030009560	S.CER C1608 CH 1H R75B-T	B	32.8/21.7
C416	4030006990	S.CER C1608 CH 1H 080D-T	B	35.3/19.3
C417	4030009350	S.CER C1608 CH 1H 3R5B-T	B	36.9/19.3
C418	4030009510	S.CER C1608 CH 1H 010B-T	B	39.3/20.1
C419	4030009540	S.CER C1608 CH 1H 1R5B-T	B	40.8/21.7
C420	4030007020	S.CER C1608 CH 1H 120J-T	B	45.6/21
C421	4030009530	S.CER C1608 CH 1H 030B-T	B	45.6/19.4
C423	4030007100	S.CER C1608 CH 1H 560J-T	B	48.5/19.4
C427	4030006860	S.CER C1608 JB 1H 102K-T	T	59/43.5
C428	4030006860	S.CER C1608 JB 1H 102K-T	T	59/41.9
C431	4030006860	S.CER C1608 JB 1H 102K-T	T	145.3/25.3
C432	4030006860	S.CER C1608 JB 1H 102K-T	T	132.3/31.6
C433	4030006860	S.CER C1608 JB 1H 102K-T	B	143/33.4
C434	4030006860	S.CER C1608 JB 1H 102K-T	T	135.7/43
C435	4030006860	S.CER C1608 JB 1H 102K-T	B	143.9/40.7
C436	4030006850	S.CER C1608 JB 1H 471K-T	B	141.7/2.6
C438	4030006850	S.CER C1608 JB 1H 471K-T	T	145.6/12.8
C439	4030006860	S.CER C1608 JB 1H 102K-T	B	130.6/16
C440	4030006860	S.CER C1608 JB 1H 102K-T	B	143.7/16.3
C441	4030006860	S.CER C1608 JB 1H 102K-T	B	140.6/10.3
J1	6510023361	S.CNR 26FLT-SM2-TB (LF) (SN)	T	139.2/36.2
J2	6510022621	S.CNR 10FMN-BMTTR-A-TBT (LF) (SN)	T	141.4/8.2
J3	6510007020	CNR TMP-J01X-V6	T	
W7	7030010840	S.JMP MJP-0.2-T	T	72.7/50.9
W12	7030010860	JMP MJP-0.6	T	
EP2	6910013370	S.BEA BLM18BB221SN1D	T	71.2/21
EP3	6910002161	CAS CASE-BM7H-LF	T	

[CONNECT UNIT]

REF NO.	ORDER NO.	DESCRIPTION	M.	H/V LOCATION
Q1	1590003560	S.FET TPC6104 (TE85L,F)	T	44.2/3
Q2	1590003580	S.TR KRC404 RTK/P	T	62.7/24.9
Q3	1590003560	S.FET TPC6104 (TE85L,F)	T	49.3/8.2
Q4	1590003580	S.TR KRC404 RTK/P	T	76.6/18.3
D1	1730002360	S.ZEN MA8062-M (TX)	T	64.4/19.1
D2	1730002360	S.ZEN MA8062-M (TX)	T	75.5/16.1
R1	7030003640	S.RES ERJ3GEYJ 473 V (47 k)	T	46.7/4.4
R2	7030003680	S.RES ERJ3GEYJ 104 V (100 k)	T	48/4.4
R3	7030003320	S.RES ERJ3GEYJ 101 V (100)	T	51.4/12.3
R4	7030003640	S.RES ERJ3GEYJ 473 V (47 k)	T	46.9/8.6
R5	7030003680	S.RES ERJ3GEYJ 104 V (100 k)	T	51.4/11
R6	7030003320	S.RES ERJ3GEYJ 101 V (100)	T	74.7/13.7
C1	4030006860	S.CER C1608 JB 1H 102K-T	T	46.8/21.2
C2	4030006860	S.CER C1608 JB 1H 102K-T	T	46.8/19.9
C8	4030006860	S.CER C1608 JB 1H 102K-T	T	18.8/19
C9	4030006860	S.CER C1608 JB 1H 102K-T	T	6.3/19.6
C11	4030006860	S.CER C1608 JB 1H 102K-T	T	35.4/5
C13	4030006860	S.CER C1608 JB 1H 102K-T	T	34.6/11.9
C16	4030006860	S.CER C1608 JB 1H 102K-T	T	44.9/6.8
C17	4030006860	S.CER C1608 JB 1H 102K-T	T	40.2/4.7
C18	4030006860	S.CER C1608 JB 1H 102K-T	T	48.5/11.9
C21	4030006860	S.CER C1608 JB 1H 102K-T	T	68.1/4.5
C23	4030006860	S.CER C1608 JB 1H 102K-T	T	63/13.1
C26	4030006860	S.CER C1608 JB 1H 102K-T	T	74.1/7.6
C27	4030006860	S.CER C1608 JB 1H 102K-T	T	69.9/4.5
C28	4030006860	S.CER C1608 JB 1H 102K-T	T	74.3/11.5
C31	4030006860	S.CER C1608 JB 1H 102K-T	T	60.5/24.9
C32	4030006860	S.CER C1608 JB 1H 102K-T	T	66.3/16.1
C35	4030006860	S.CER C1608 JB 1H 102K-T	T	76.5/20.2
C36	4030011600	S.CER C1608 JB 1E 104K-T	T	76/13.7
J1	6510022440	CNR LTW-8MP-C NUT,GASKET		
J2	6510022440	CNR LTW-8MP-C NUT,GASKET		

[CONNECT UNIT]

REF NO.	ORDER NO.	DESCRIPTION	M.	H/V LOCATION
J3	6510023320	CNR LTWD-06PMMMP-LC <LIA>!		
J4	6510023091	S.CNR 20FLT-SM2-TB (LF) (SN)	T	54.5/20
J5	6510014961	S.CNR B2B-ZR-SM4-TF (LF) (SN)	T	12.9/21.3
J6	6510022691	S.CNR 06FLT-SM2-TB (LF) (SN)	T	70/19.7
J6	6510025820	S.CNR 06FLZT-SM1-TF (H) (LF)(SN)	T	70/19.7
F1	5210001000	S.FUS ERBSE2R50U	T	37.4/3.7
F2	5210001000	S.FUS ERBSE2R50U	T	62.5/5.6
W1	8900015560	CBL OPC-1659 (P0.5,N6,L140) <TJM>		

[DIAL UNIT]

REF NO.	ORDER NO.	DESCRIPTION	M.	H/V LOCATION
S1	2250000470	ECR EVQ-V9C00116E		
W1	8900011721	CBL OPC-1187A <TJM>		

[VR UNIT]

REF NO.	ORDER NO.	DESCRIPTION	M.	H/V LOCATION
R1	7210003080	VAR TP96N97-15F-10KA-2345		
W1	8900015960	CBL OPC-1186A <TJM>		

[SQL UNIT]

REF NO.	ORDER NO.	DESCRIPTION	M.	H/V LOCATION
R1	7210002360	VAR TP96N97-15F-10KB-1301		

M.=Mounted side (T: Mounted on the Top side, B: Mounted on the Bottom side)
S.=Surface mount

SECTION 7 MECHANICAL PARTS

7-1 IC-M603

[CHASSIS PARTS]

REF. NO.	ORDER NO.	DESCRIPTION	QTY.
J1	6510004880	Antenna connector MR-DSE-01	1
W1	8900015950	Cable OPC-1175A	1
W2	8900011460	Cable OPC-1176	1
W3	8900011161	Cable OPC-1128A	1
W4	8900011811	Cable OPC-1197A-1	1
W5	8900011821	Cable OPC-1198A-1	1
W6	8900011831	Cable OPC-1200A-1	1
MP1	8010020510	2577 A-chassis	1
MP2	8210019050	2577 rear panel assembly	1
MP3	8930058271	2577 F-packing-1	1
MP4	8930058260	2577 R-packing	1
MP5	8930058460	2577 bush plate	1
MP6	8110007800	2577 module cover	1
MP7	8930058480	2577 module plate	1
MP8	8510014900	2577 shield cover	1
MP9	8930034300	1542 ANT seal	1
MP10	8930058780	2577 sheet	1
MP11	8810008661	Screw B0 3 × 8 NI-ZC3 (BT)	7
MP12	8810008661	Screw B0 3 × 8 NI-ZC3 (BT)	3
MP13	8820001210	2438 screw	6
MP14	8820001210	2438 screw	6
MP15	8820001210	2438 screw	2
MP16	8820001210	2438 screw	2
MP17	8810004301	Screw M3 × 10 ZK3	2
MP19	8930052290	O-ring (AD)	8
MP20	8930058450	2577 IC clip	1
MP21	8930058470	2577 IC plate	1
MP22	8930049040	Insulation plate (FQ)	1
MP23	8930055040	2438 cap	5
MP25	8930059021	Thermally sheet (AI)-1	1
MP26	8810008661	Screw B0 3 × 8 NI-ZC3 (BT)	1

[MAIN UNIT]

REF. NO.	ORDER NO.	DESCRIPTION	QTY.
EP3	6910002161	Shield case-BM7H-LF	1
MP1	8510014870	2577 RX-VCO case	1
MP2	8510014880	2577 TX-VCO case	1
MP3	8510014890	2577 DBM case	1
MP4	8510014890	2577 DBM case	1
MP5	8510015100	2577 shield case	1
MP6	8930059350	2577 earth plate	1
MP9	8930063240	2577 earth spring	1
MP10	8930071140	2577 A-earth plate	1

[LOGIC UNIT]

REF. NO.	ORDER NO.	DESCRIPTION	QTY.
DS1	5030002390	LCD HLM7784-010100	1
S1	2230001170	Switch SPPH220200	1
MP1	8930058440	2577 LCD holder	1
MP2	8210019060	2577 reflector	1
MP3	8930058910	2577 LCD sheet	1
MP4	8930058900	Rubber sheet (BB)	2

[AF UNIT]

REF. NO.	ORDER NO.	DESCRIPTION	QTY.
W6	8900011871	Cable OPC-1203A	1

Screw abbreviations B0, BT: Self-tapping PH: Pan head
 FH: Flat head ZK: Black
 SUS: Stainless NI: Nickel
 NI-ZU: Nickel-Zinc

[FRONT UNIT]

REF. NO.	ORDER NO.	DESCRIPTION	QTY.
SP1	2510001210	SPEAKER 066P0804	1
W1	8900015550	Cable OPC-1658	1
MP1	8210023110	2577 front panel (C) assembly	1
MP12	8610011240	Knob N-295	1
MP13	8610011240	Knob N-295	1
MP14	8610011210	Knob N-293	1
MP17	8930059540	Compression ring R102-P	1
MP18	8810008661	Screw B0 3 × 8 NI-ZC3 (BT)	7
MP19	8930059200	O-ring (AT)	1
MP20	8930053030	2345 earth plate	1
MP21	8850001981	Plain washer M10 10 × 16 × 0.8 ZC3	1
MP22	8930052280	O-ring (AC)	2
MP23	8930058790	2577 SP NET	1

[CONNECT UNIT]

REF. NO.	ORDER NO.	DESCRIPTION	QTY.
J1	6510022440	Connector LTW-8MP-C	1
J2	6510022440	Connector LTW-8MP-C	1
J3	6510023620	Connector LTWBU-10MPMP-LC	1

[DIAL UNIT]

REF. NO.	ORDER NO.	DESCRIPTION	QTY.
S1	2250000470	Switch EVQ-V9C00116E	1

[SQL UNIT]

REF. NO.	ORDER NO.	DESCRIPTION	QTY.
R1	7210002360	Variable resistor TP96N97-15F-10KA-1301	1

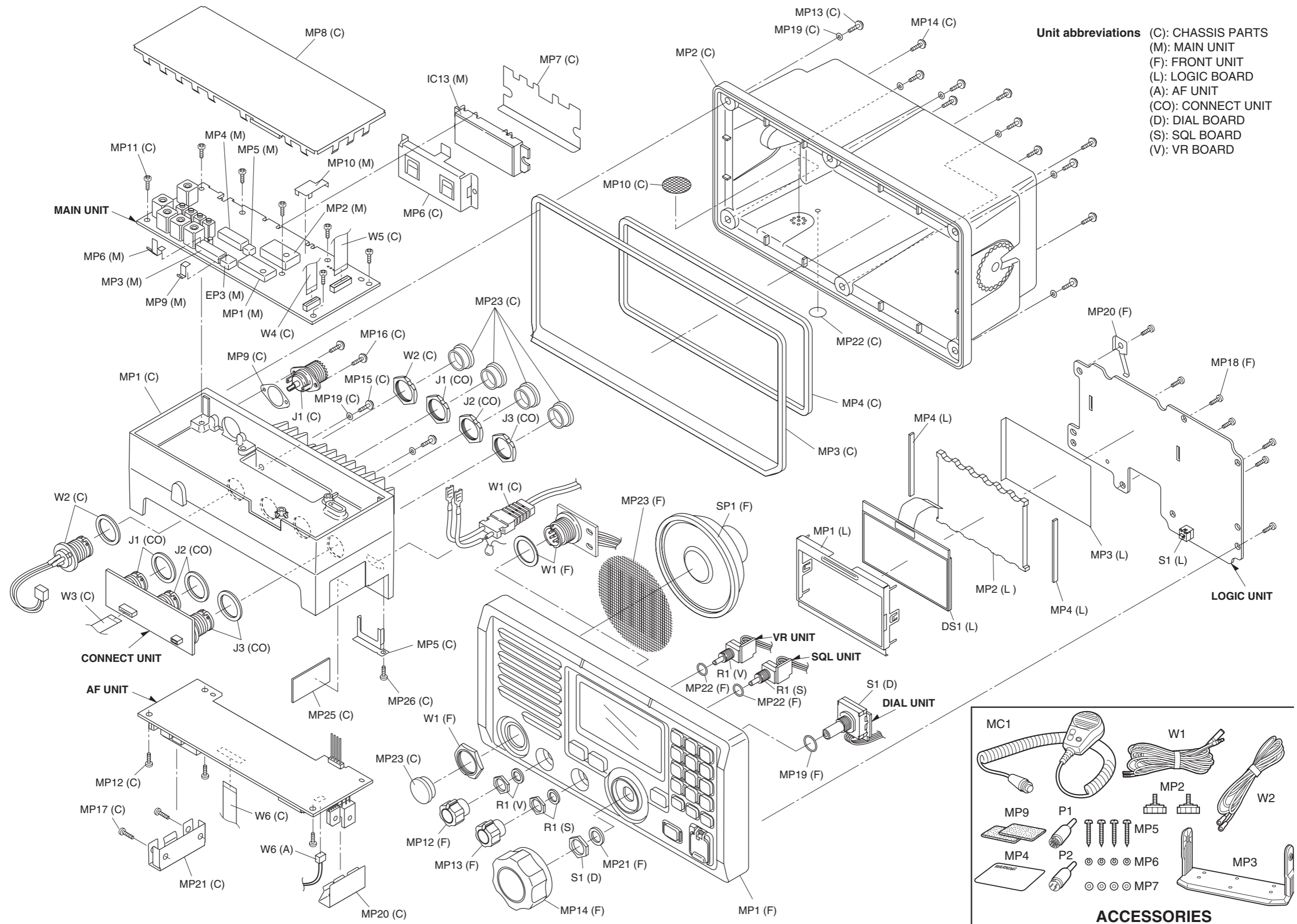
[VR UNIT]

REF. NO.	ORDER NO.	DESCRIPTION	QTY.
R1	7210003080	Variable resistor TP96N97-15F-10KA-2345	1

[ACCESSORIES]

REF. NO.	ORDER NO.	DESCRIPTION	QTY.
W1	8900011440	Cable OPC-1174	1
W2	8900010761	Cable OPC-1096A	1
P1	6510023310	LTWD-06BFFA-L180	1
P2	6510023300	LTWB-03BFFA-L180	1
MC1	Optional product	HM-126RB	1
MP1	8950005110	2289 mic hanger	1
MP2	8610010561	2040 knob bolt-1	2
MP3	8010018860	2455 bracket	1
MP4	8310050900	2438 warning sticker	1
MP5	8810001490	Screw PH A M5 × 20 SUS	4
MP6	8850000500	Spring washer M5 SUS	4
MP7	8850000180	Flat washer M5 SUS	4
MP8	8810004700	Screw PH A M3 × 16 SUS	2
MP9	8930059480	Sponge (GZ)	2

Accessories illustration is shown at the next page.



7-2 HM-126RB/RG

[CHASSIS PARTS]

REF. NO.	ORDER NO.	DESCRIPTION	QTY.
W1	8900011480	Cable OPC-948 [Gray]	1
	8900011490	Cable OPC-949 [Black]	1
MP1	8210017670	2352 front panel-1 [Gray]	1
	8210019241	2352 front panel (A)-1 [Black]	1
MP2	8210016851	2352 rear panel [Gray]	1
	8210019221	2352 rear panel (A) [Black]	1
MP3	8930052160	2352 key	1
MP4	8930052150	2352 PTT rubber	1
MP5	8930052140	2352 PTT holder	1
MP6	8930052690	2352 mic rubber	1
MP7	8930052120	2352 rubber	1
MP8	8930052110	2352 main seal	1
MP9	8610010870	2352 hanger knob	1
MP10	8310048760	2352 R-plate	1
MP11	8310048780	2352 mic plate	1
MP12	8820001150	2352 screw	5
MP13	8850001850	ICOM washer (Y)	5
MP14	8930052340	O-ring (AE)	5
MP15	8930052350	O-ring (AF)	1
MP16	8930053870	2352 sheet (A)	1
MP17	8930053040	2352 SP net	1
MP18	8850001610	Spring washer M4 SUS	1
MP19	8810009260	Screw PH BT 2 × 6 NI	3
MP20	8810008900	Screw PH M3 × 6 NI	1
MP21	8810009240	Screw M4 × 10 ZK	1
MP24	8930061010	2715 mic sheet	1

[MAIN UNIT]

REF. NO.	ORDER NO.	DESCRIPTION	QTY.
MC1	7700002120	Microphone KUC2123-030245	1
SP1	2510001080	Speaker S36G04K-9	1

Screw abbreviations

BT: Self-tapping	PH: Pan head
FH: Flat head	ZK: Black
SUS: Stainless	NI: Nickel
NI-ZU: Nickel-Zinc	

7-3 HM-157 (Optional product)

[CHASSIS PARTS]

REF. NO.	ORDER NO.	DESCRIPTION	QTY.
W3	8900010220	Cable OPC-1412 [B]	1
	8900010230	Cable OPC-1413 [SW]	1
SP1	2510001330	Speaker 036D0803	1
MP1	8210017360	2417 front panel (C)-1 assembly [B]	1
	8210017390	2417 front panel (D)-1 assembly [SW]	1
MP3	8930053510	2417 key board	1
MP4	8930053540	2417 PTT rubber	1
MP5	8930053990	2417 PTT sheet	1
MP6	8930053550	2417 PTT holder	1
MP7	8930053520	2417 top key	1
MP8	8930053760	2417 key plate	1
MP10	8930061110	2681 mic tape	1
MP11	8810009180	Screw FH BT M2 × 5 NI-ZU	3
MP12	8930053750	2417 plate	1
MP13	8210017250	2417 rear panel-1 [B]	1
	8210022021	2417 rear panel (C)-1 [SW]	1
MP14	8310049970	2417 R-plate	1
MP15	8610010980	2417 hanger knob	1
MP16	8930053740	O-ring (AJ)	1
MP17	8930008450	Screw M4 × 8 ZK	1
MP18	8850000490	Spring washer M4 SUS	1
MP19	8930053530	2417 rubber	1
MP20	8820001180	2417 screw 2.6 × 16	6
MP21	8930053730	O-ring (AI)	6
MP25	8930046000	1903 mic sponge	1
MP26	8930061610	Sponge (HG)	1
MP27	8930039000	1757 sheet	1

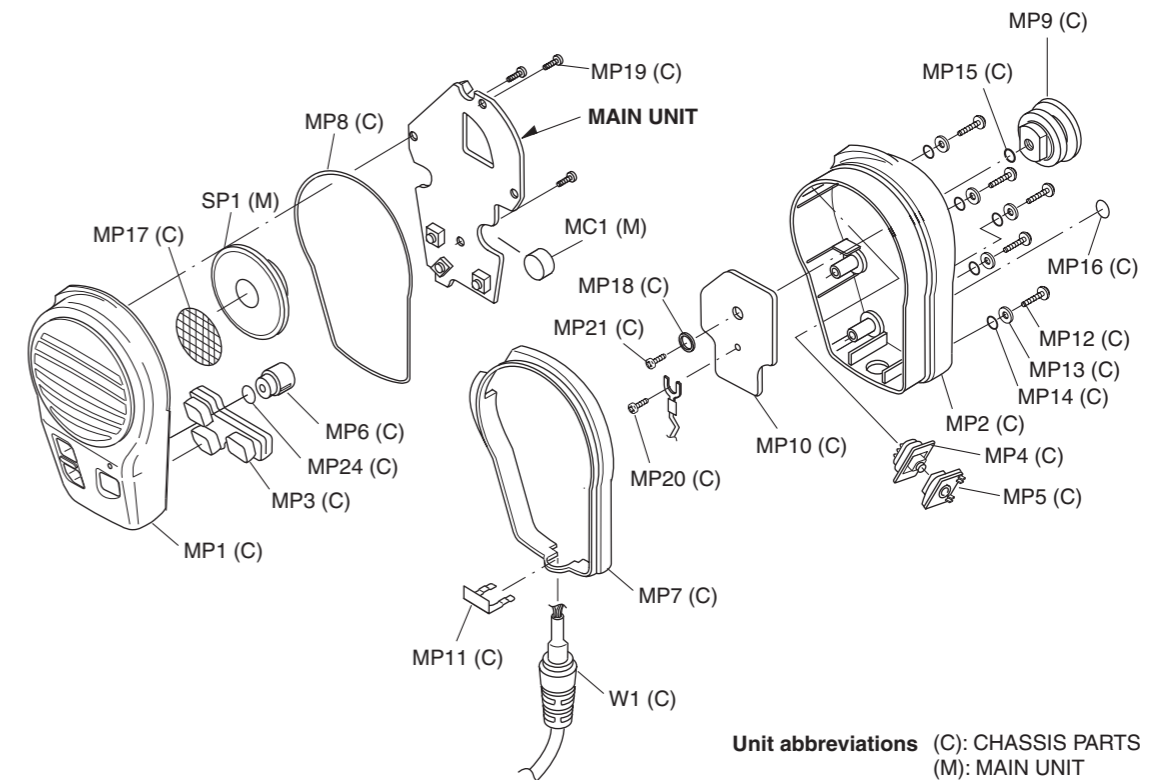
[MAIN UNIT]

REF. NO.	ORDER NO.	DESCRIPTION	QTY.
DS1	5030001900	LCD A0119 (LCD 36 × 28.5)	1
EP2	8930051120	LCD contact SRCN-2320-SP-N-W	2
MC1	7700002480	Microphone SKB-2746 LPC	1
MP1	8930053780	2417 LCD holder	1
MP2	8210017240	2417 reflector	1
MP3	8310050200	2417 H-plate	1
MP4	8930039612	Thermally sheet (C)-2	1
MP5	8930054890	Insulate sheet (GM)	2

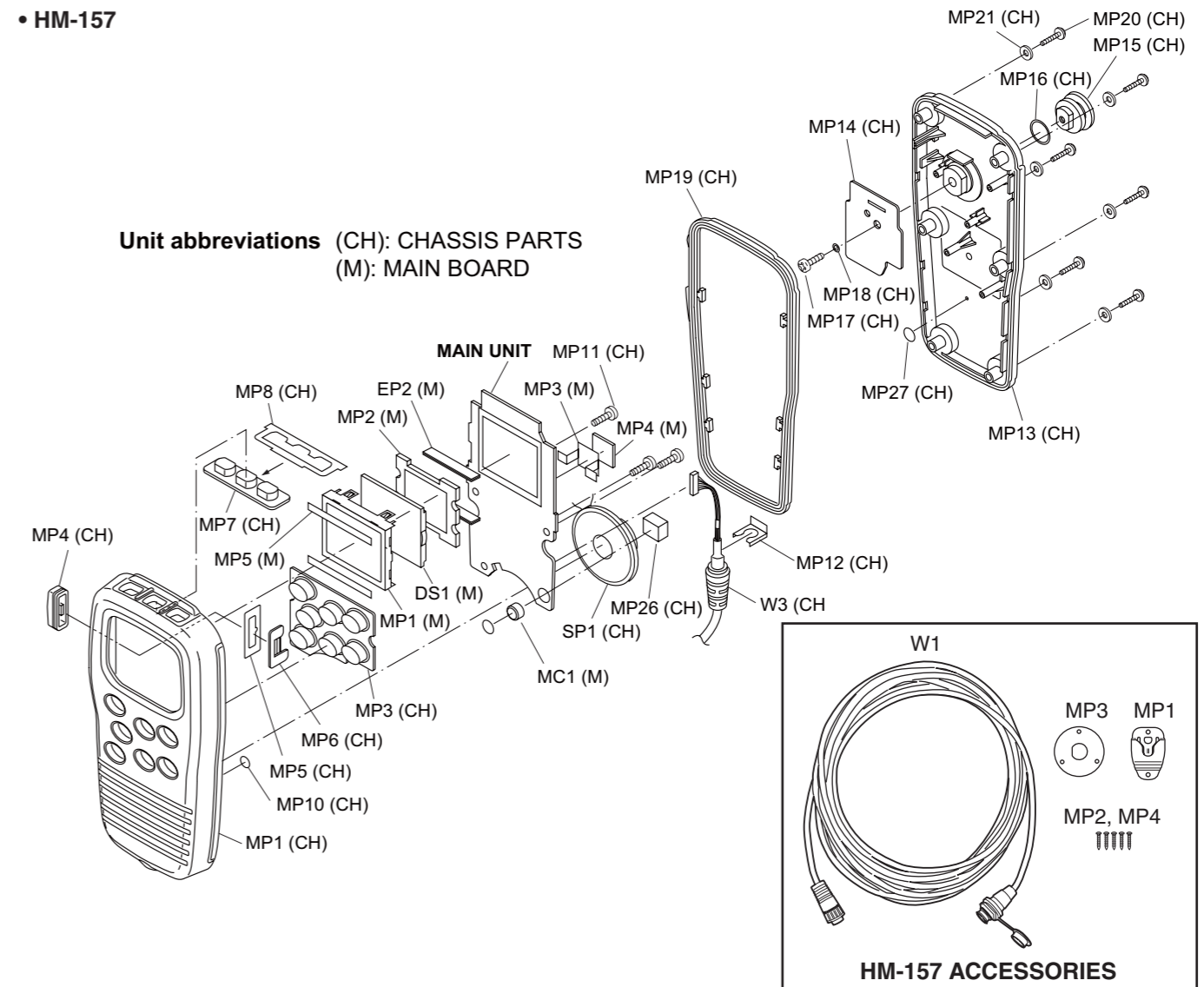
[ACCESSORIES]

REF. NO.	ORDER NO.	DESCRIPTION	QTY.
W1	8900010280	Cable OPC-1000	1
MP1	8950005110	2289 mic hanger	1
MP2	8810004700	Screw PH A M3 × 16 SUS	2
MP3	8310050320	2417 C-plate	1
MP4	8810004700	Screw PH A M3 × 16 SUS	3

• HM-126RB/RG

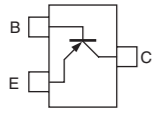
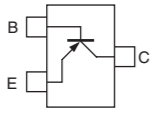
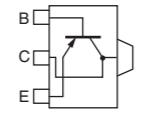
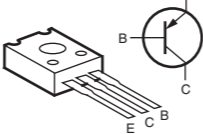
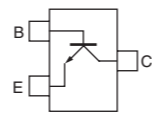
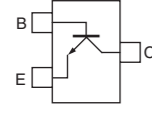
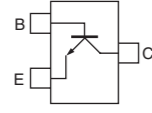
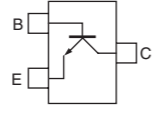
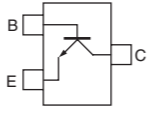
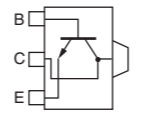
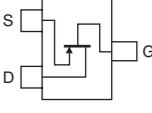
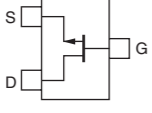
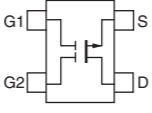
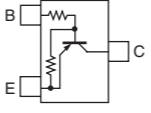
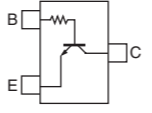
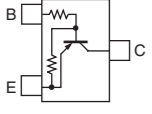
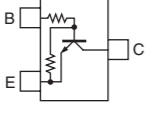
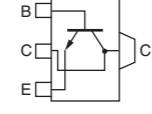
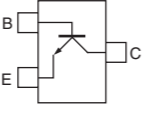
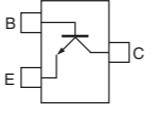
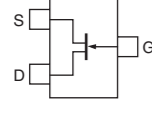
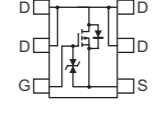


• HM-157


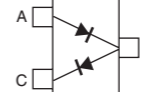
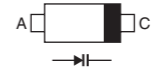
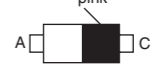


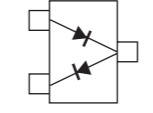
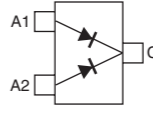
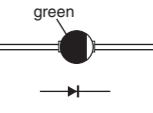
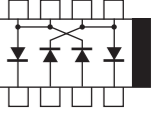
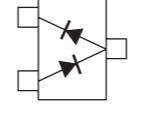


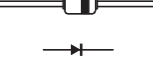

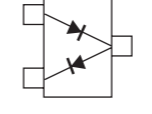


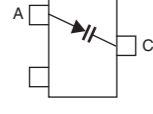



SECTION 8 SEMI-CONDUCTOR INFORMATION

• TRANSISTORS AND FET'S

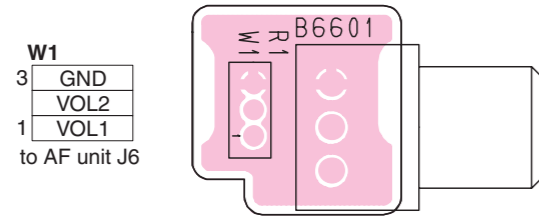
2SA1576A T106 R (Symbol: FR) 	2SA1577 T106 Q (Symbol: HQ) 	2SB1132 T100 R (Symbol: BAR) 	2SB1143 S (Symbol: B1143) 	2SC3326 B (Symbol: CCB) 
2SC3775 3 TB (Symbol: OY3) 	2SC4081 T106 S (Symbol: BS) 	2SC4116 BL (Symbol: LL) 	2SC4215 O (Symbol: QO) 	2SD1664 Q (Symbol: DAQ) 
2SJ144 Y (Symbol: VY) 	2SK1069 4 TL (Symbol: FJ) 	3SK294 (Symbol: UV) 	DTA144EUA T106 (Symbol: 16) 	DTC144TU T106 (Symbol: 06) 
KRA304 (Symbol: PD) 	KRC404 (Symbol: ND) 	KTA1664 (Symbol: R) 	KTC2875-B (Symbol: MB) 	KTC4075 BL (Symbol: LBL) 
PMBFJ310 (Symbol: M10) 	TPC6104 (Symbol: S3D) 			

• DIODES

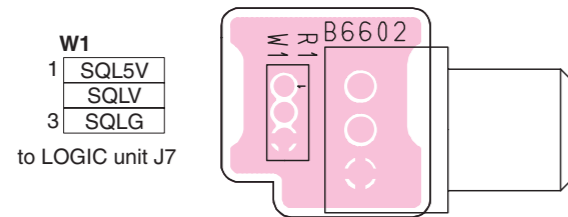
1SS355 (Symbol: A) 	1SS372 (Symbol: N9) 	1SV214 (Symbol: T1) 	1SV288 (Symbol: TJ) 	1SV307 (Symbol: TX) 
1SV308 (Symbol: TX) 	DA204 K T146 (Symbol: K) 	DAN202 K T146 (Symbol: N) 	DSA3A1 (Color: Green) 	HSB88WSTR (Symbol: Silver line) 
HSM88ASR TR (Symbol: C3) 	HVC358B (Symbol: B2) 	KDS4148U (Symbol: UH) 	L308CCB (Symbol: CC) 	MA2S111 (Symbol: A) 
MA742 (Symbol: M1U) 	MA77 (Symbol: 4B) 	MA8036 L (Symbol: 3_6) 	MA8051 M (Symbol: 5-1) 	MA8062 M (Symbol: 6-2) 

SECTION 9 BOARD LAYOUTS

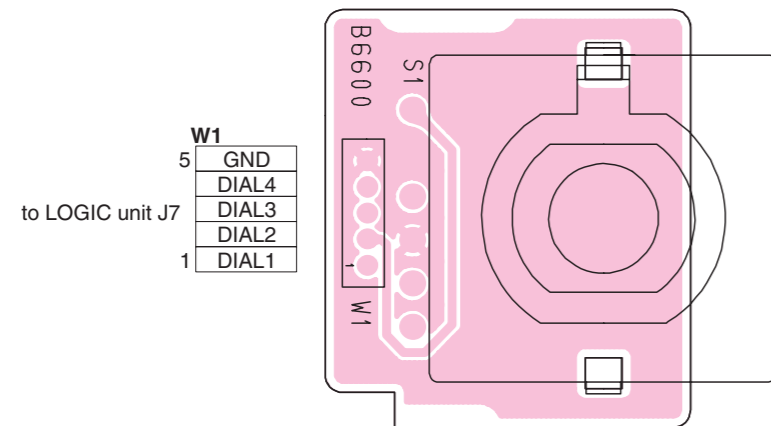
9-1 VR UNIT (TOP VIEW)



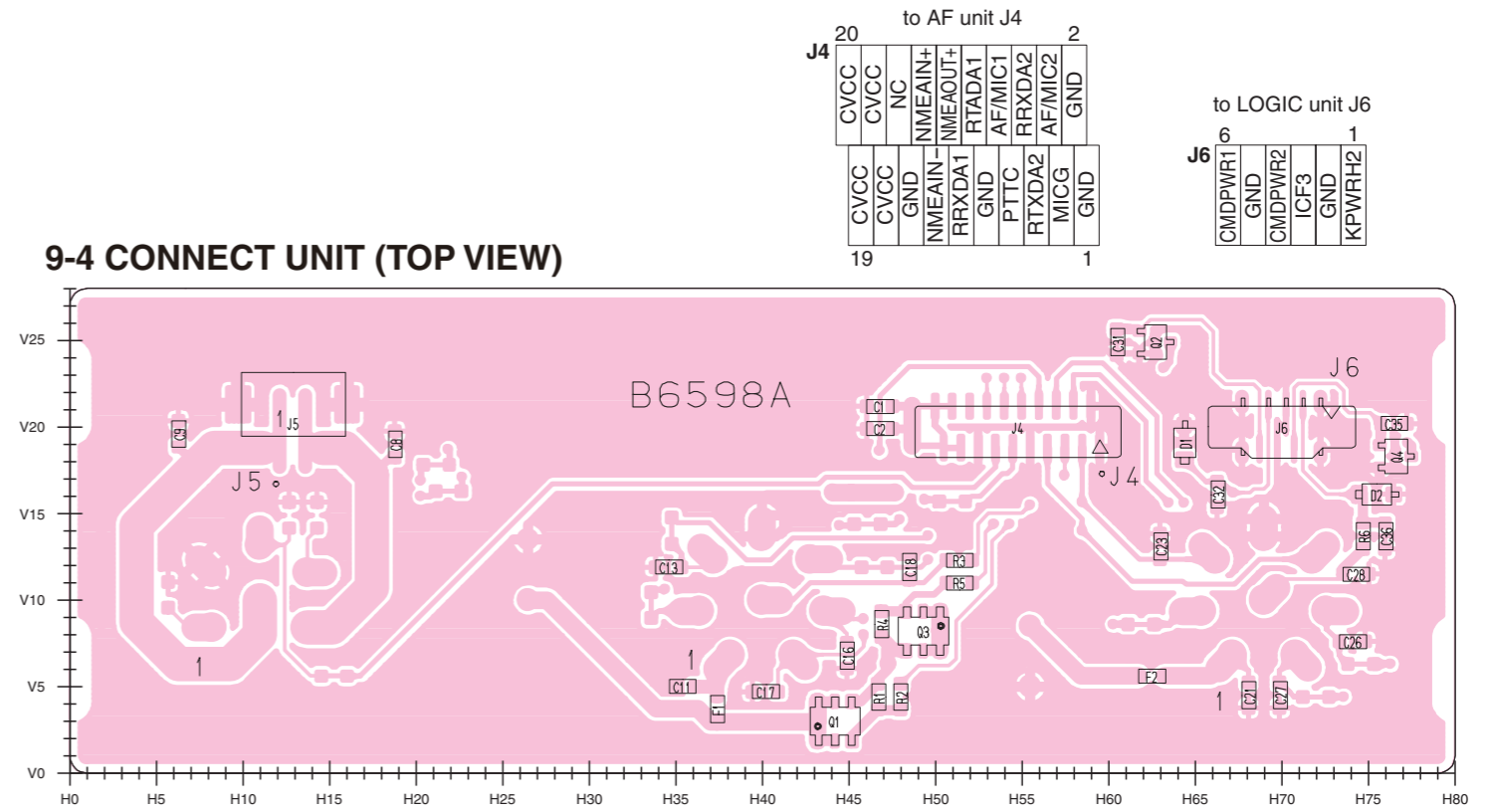
9-2 SQL UNIT (TOP VIEW)



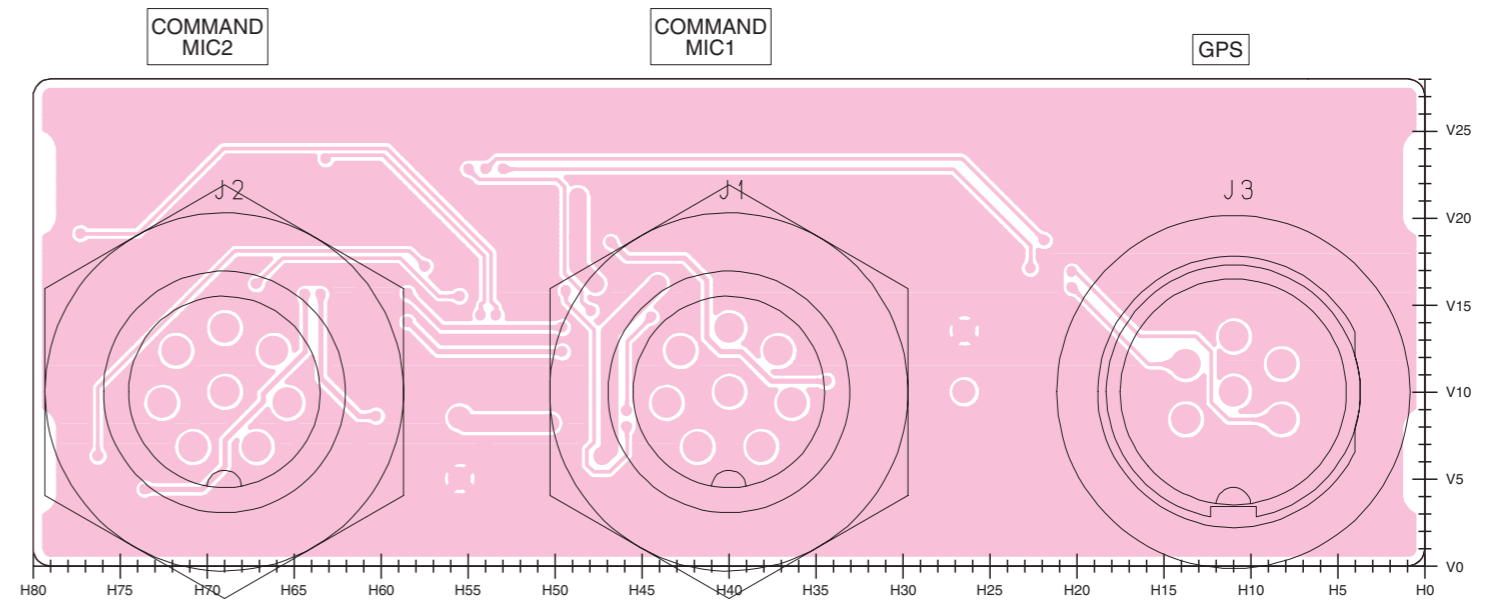
9-3 DIAL UNIT (TOP VIEW)



9-4 CONNECT UNIT (TOP VIEW)

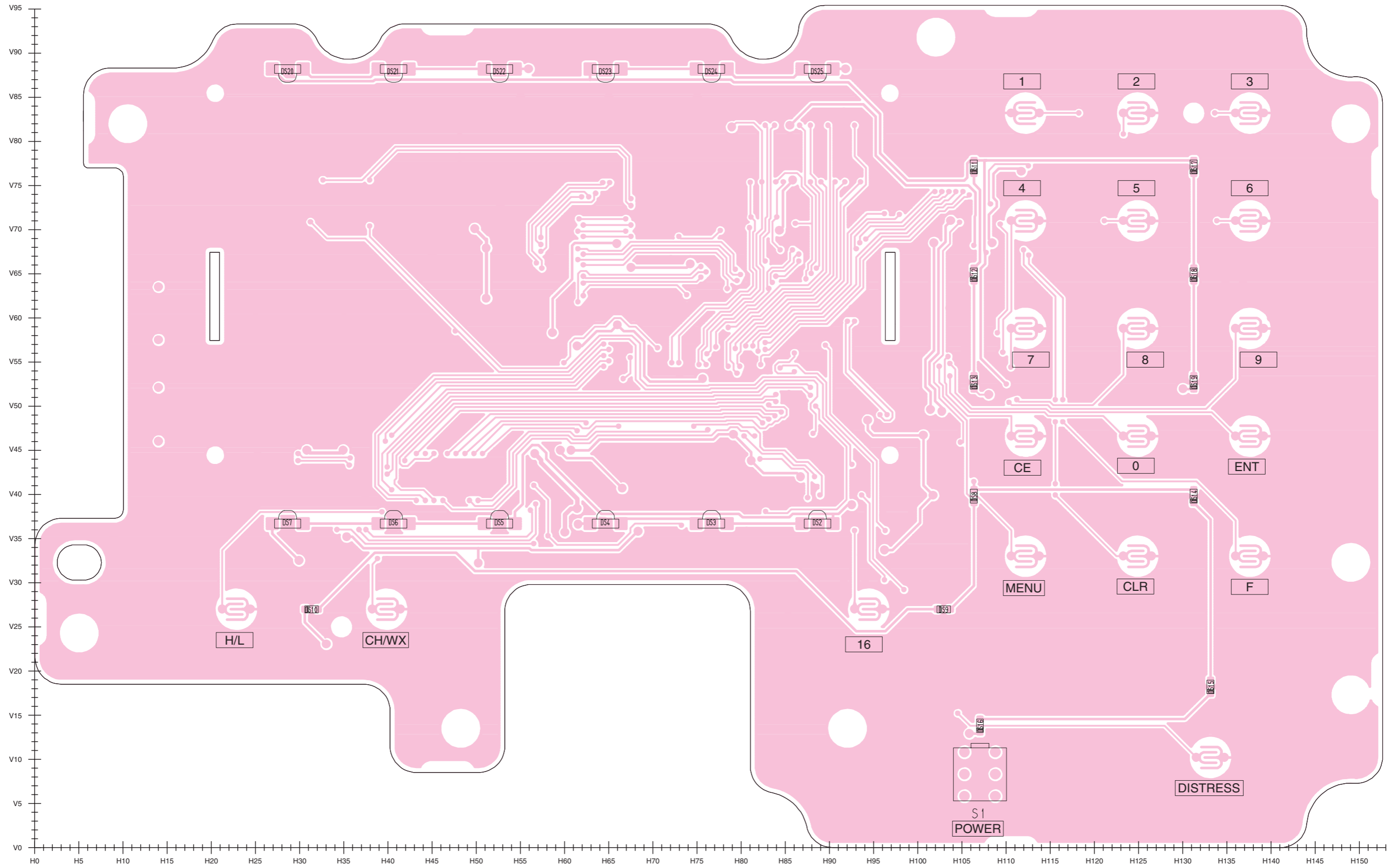


• CONNECT UNIT (BOTTOM VIEW)



The combination of this page and the next page shows the unit layout in the same configuration as the actual P.C. Board.

9-5 LOGIC UNIT (TOP VIEW)



9-5 LOGIC UNIT (BOTTOM VIEW)

The combination of this page and the previous page shows the unit layout in the same configuration as the actual P.C. Board.

J1 to MAIN unit J1

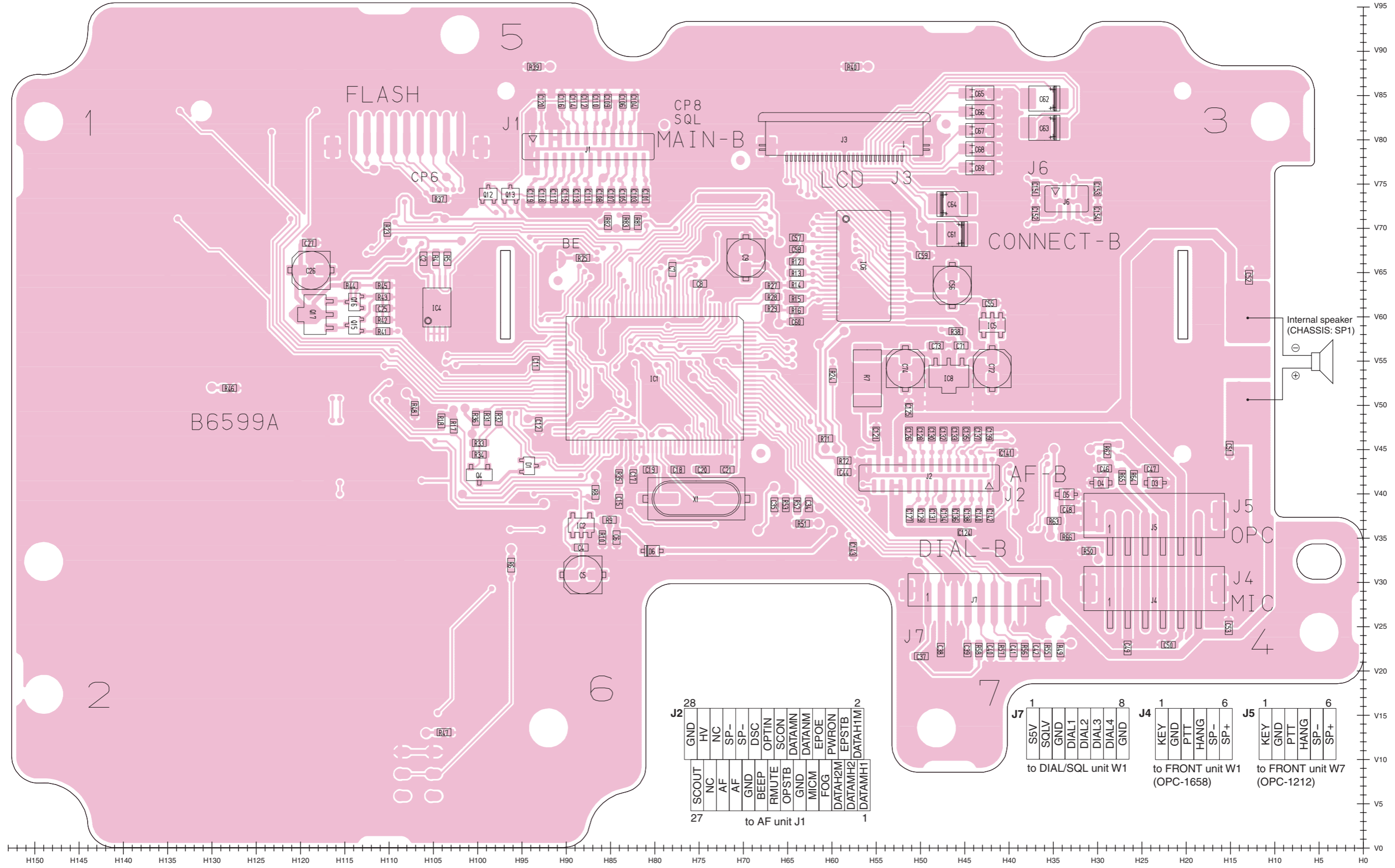
1	GND	25
2	TXDET	
3	GND	
4	SEND	
5	DASTB	
6	TMUTE	
7	H/L	
8	ATT1	
9	UNLK	
10	PDATA	
11	GND	
12	S5V	
13	DEC2	
14	DEC1	
15	SOL	
16	WXDET	
17	GND	
18	FXADJ	
19	SOL	
20	TEMP	
21	GND	
22	TXDET	
23	GND	
24	SEND	
25	DASTB	
26	TMUTE	

J3 to LCD

27	CS1B	1
28	RESETB	
29	RS	
30	RW_WR	
31	E_RD	
32	DB0	
33	DB1	
34	DB2	
35	DB3	
36	DB4	
37	DB5	
38	DB6	
39	DB7	
40	GND	
41	VDD	
42	VOUT	
43	C3+	
44	C3-	
45	C1+	
46	C1-	
47	C2+	
48	C2-	
49	V0	
50	V1	
51	V2	
52	V3	
53	V4	

J6 to CONNECT unit J6

1	CMDPWR1	5
2	GND	
3	ICF3	
4	GND	
5	CMDPWR2	
6	KPWRH2	



J2 to AF unit J1

27	SCOUT	1
28	NC	
29	AF	
30	AF	
31	SP-	
32	SP-	
33	DSC	
34	BEEP	
35	RMUTE	
36	OPSTB	
37	GND	
38	MICM	
39	FOG	
40	PWRON	
41	DATAH2M	
42	DATAH2	
43	DATAH1M	
44	DATAH1	

J7 to DIAL/SQL unit W1

1	S5V	8
2	SQLV	
3	GND	
4	DIAL1	
5	DIAL2	
6	DIAL3	
7	DIAL4	
8	GND	

J4 to FRONT unit W1 (OPC-1658)

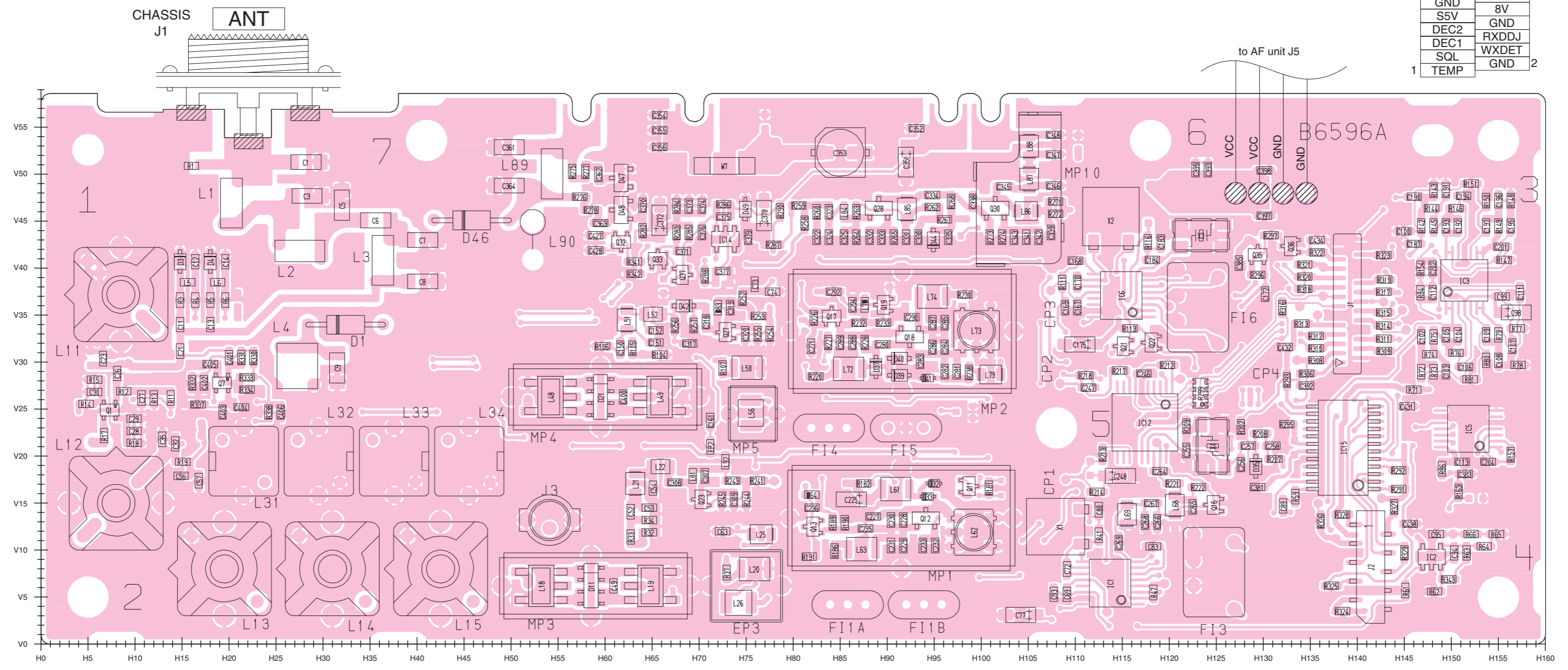
1	KEY	6
2	GND	
3	PTT	
4	HANG	
5	SP-	
6	SP+	

J5 to FRONT unit W7 (OPC-1212)

1	KEY	6
2	GND	
3	PTT	
4	HANG	
5	SP-	
6	SP+	

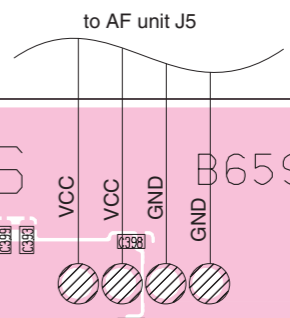
The combination of this page and the next page shows the unit layout in the same configuration as the actual P.C. Board.

9-6 MAIN UNIT (TOP VIEW)



to LOGIC unit J1

25	J1	GND	GND	26
		SEND	TXDET	
		DADATA	GND	
		TMUTE	GND	
		ATT2	H/L	
		UNLK	ATT1	
		PDATA	PSTB	
		GND	PCK	
		S5V	8V	
		DEC2	GND	
		DEC1	RXDDJ	
		SQL	WXDET	
1		TEMP	GND	2

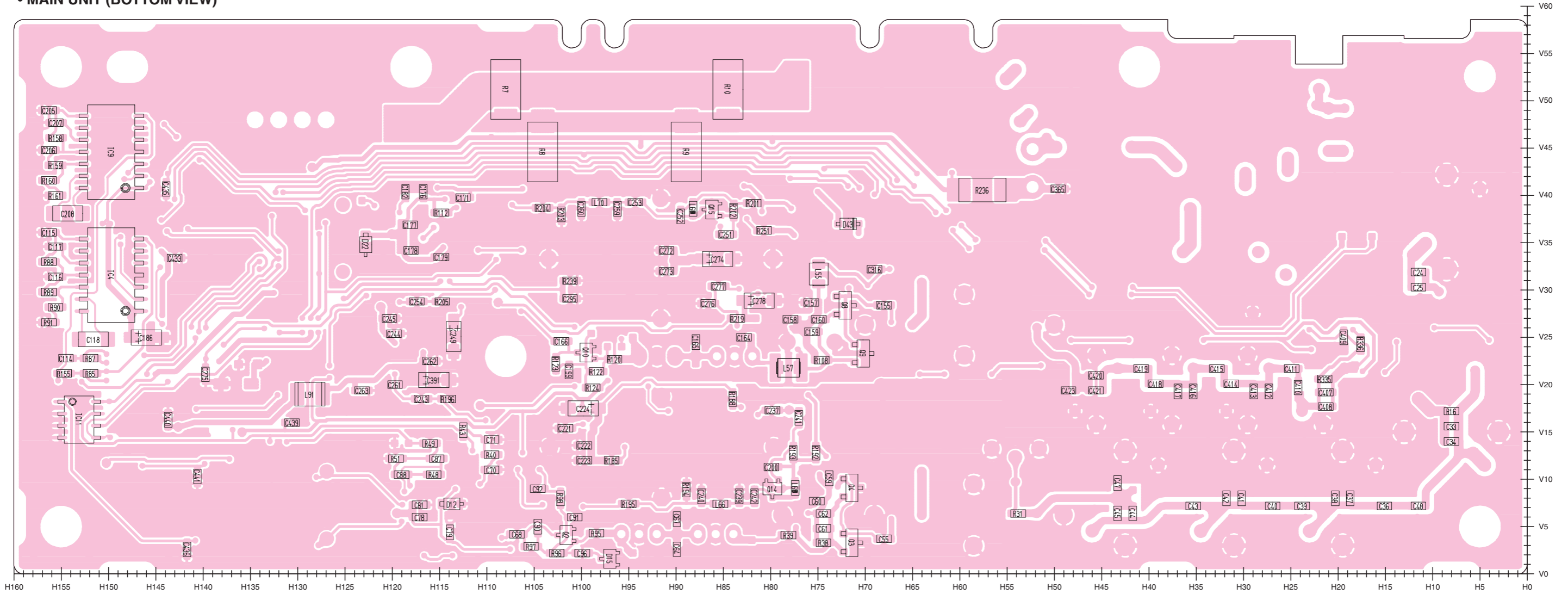


to AF unit J2

1	J2	8V	S5V	2
		R8	PCK	
		PDATA	HLVL	
		MOD	NWC	
9		DET	GND	10

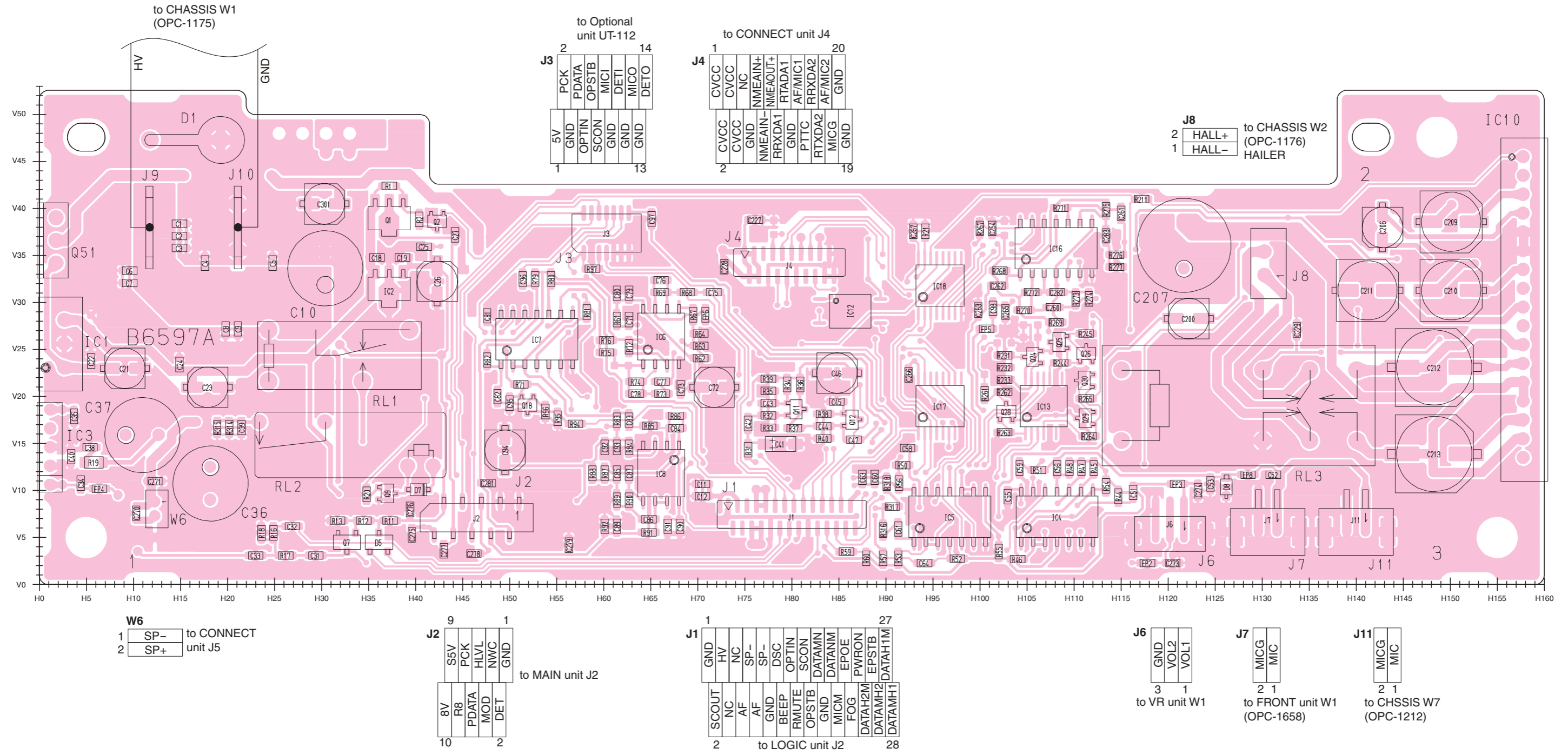
The combination of this page and the previous page shows the unit layout in the same configuration as the actual P.C. Board.

• MAIN UNIT (BOTTOM VIEW)



The combination of this page and the next page shows the unit layout in the same configuration as the actual P.C. Board.

9-7 AF UNIT (TOP VIEW)



to CHASSIS W1
(OPC-1175)

to Optional unit UT-112

2	PCK	14
1	GND	13
3	OPTIN	
4	SCON	
5	GND	
6	GND	
7	GND	
8	DETO	

to CONNECT unit J4

1	CVCC	20
2	CVCC	19
3	NC	
4	NMEAIN+	
5	NMEAIN-	
6	RRXDA1	
7	RTADA1	
8	GND	
9	AF/MIC1	
10	PTTC	
11	RRXDA2	
12	AF/MIC2	
13	MICG	
14	GND	

J8 to CHASSIS W2 (OPC-1176) HAILER

2	HALL+
1	HALL-

W6 to CONNECT unit J5

1	SP-
2	SP+

J2 to MAIN unit J2

9	S5V	1
8	PCK	
7	HLVL	
6	NWC	
5	GND	
4	MOD	
3	DET	
2	GND	
10	8V	

J1 to LOGIC unit J2

1	GND	27
2	SCOUT	28
3	NC	
4	AF	
5	AF	
6	SP-	
7	DSC	
8	BEEP	
9	RMUTE	
10	OPTIN	
11	SCON	
12	OPSTB	
13	GND	
14	DATANM	
15	DATANM	
16	MICM	
17	FOG	
18	PWRON	
19	DATAH2M	
20	DATAH2	
21	EPSTB	
22	DATAH1M	
23	DATAH1	

J6 to VR unit W1

3	GND
2	VOL2
1	VOL1

J7 to FRONT unit W1 (OPC-1658)

2	MICG
1	MIC

J11 to CHSSIS W7 (OPC-1212)

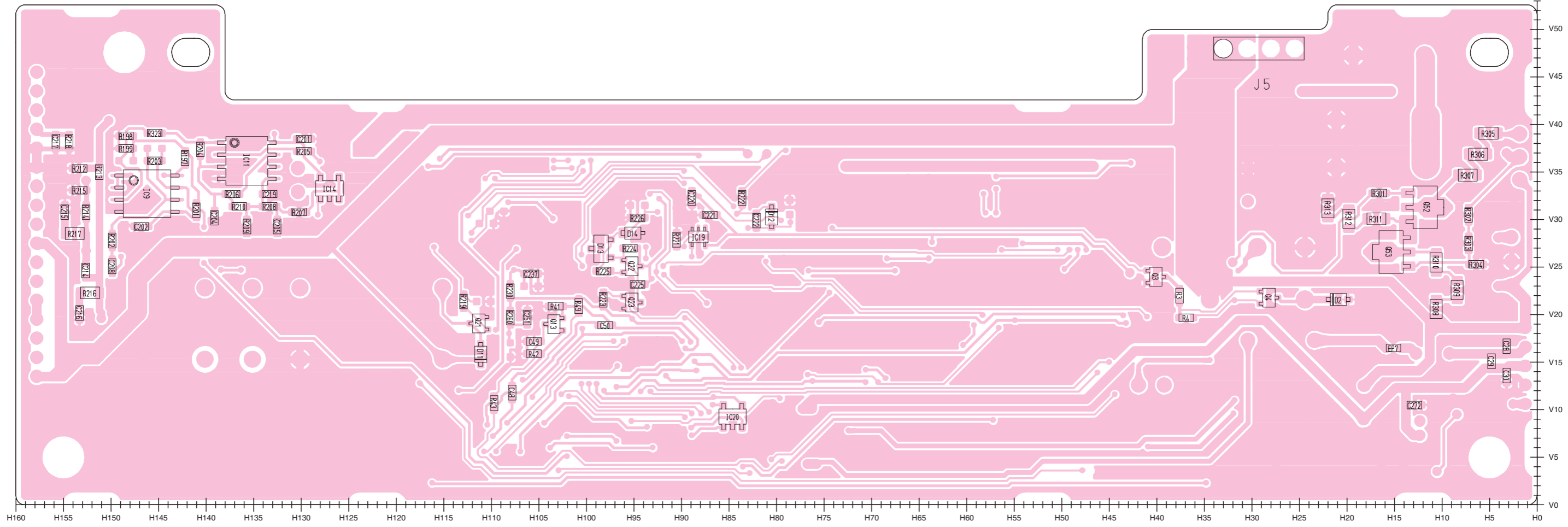
2	MICG
1	MIC

The combination of this page and the previous page shows the unit layout in the same configuration as the actual P.C. Board.

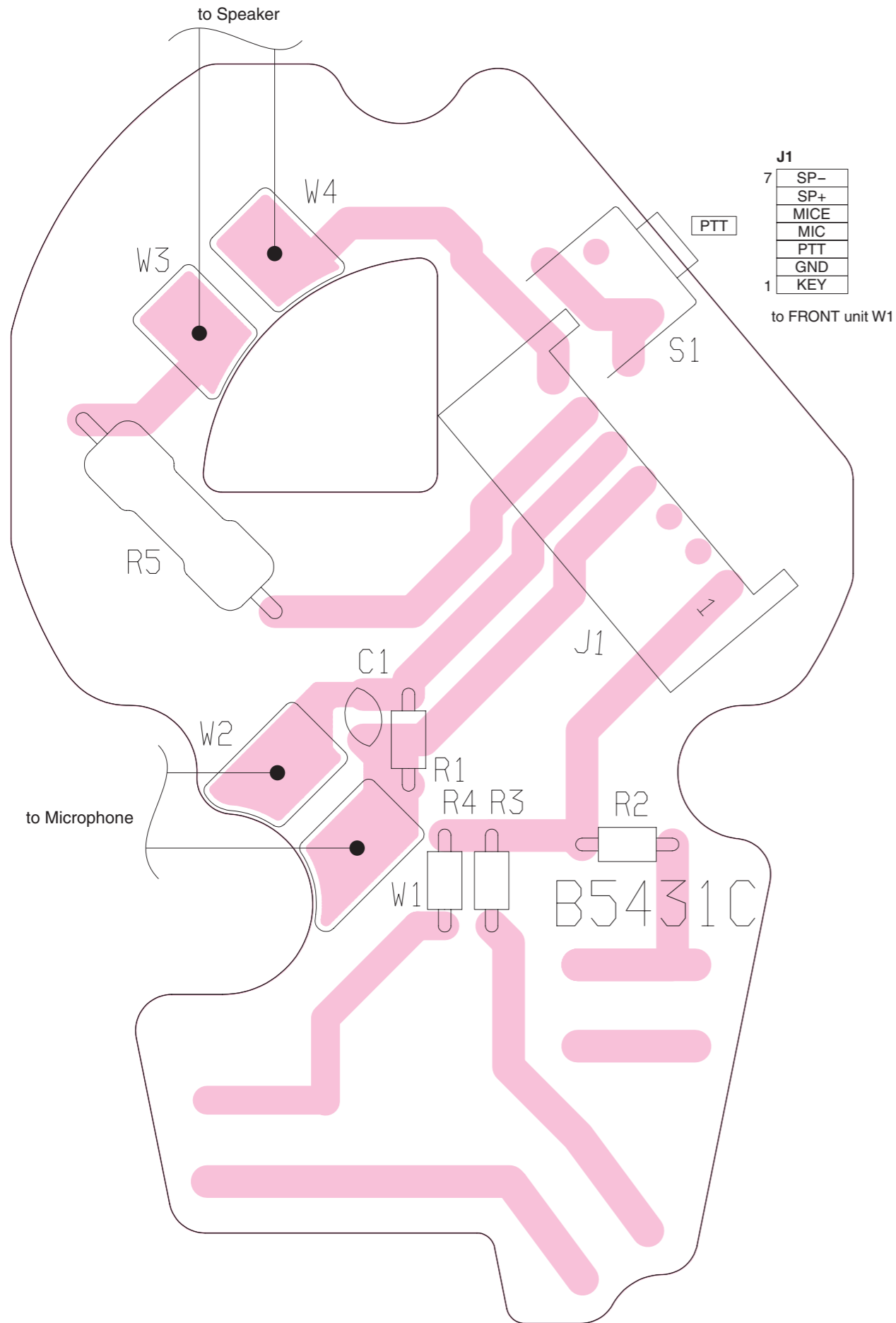
• AF UNIT (BOTTOM VIEW)

J5	
VCC	1
VCC	2
GND	3
GND	4

to MAIN unit

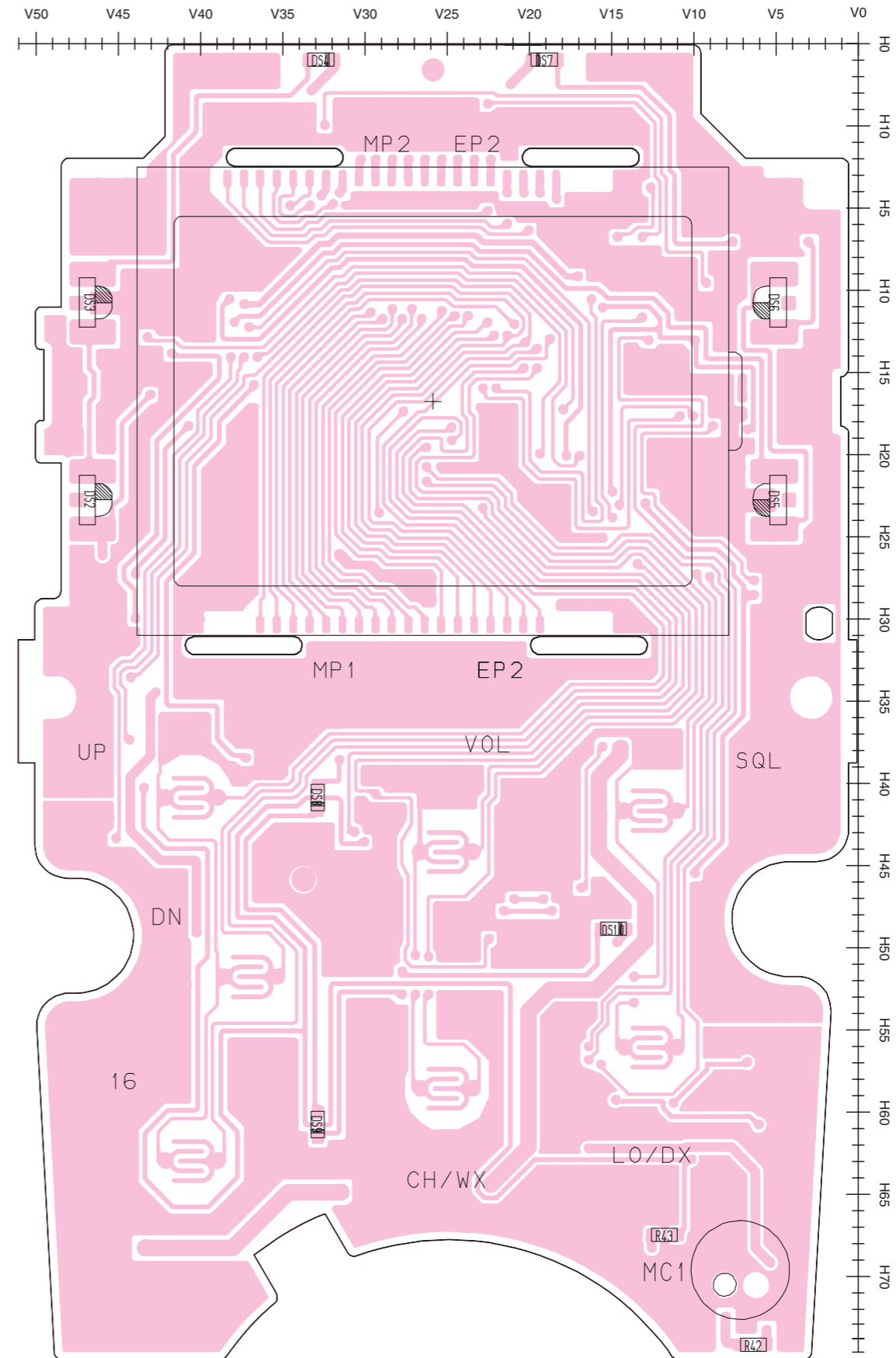


9-8 HM-126RB/RG (TOP VIEW)



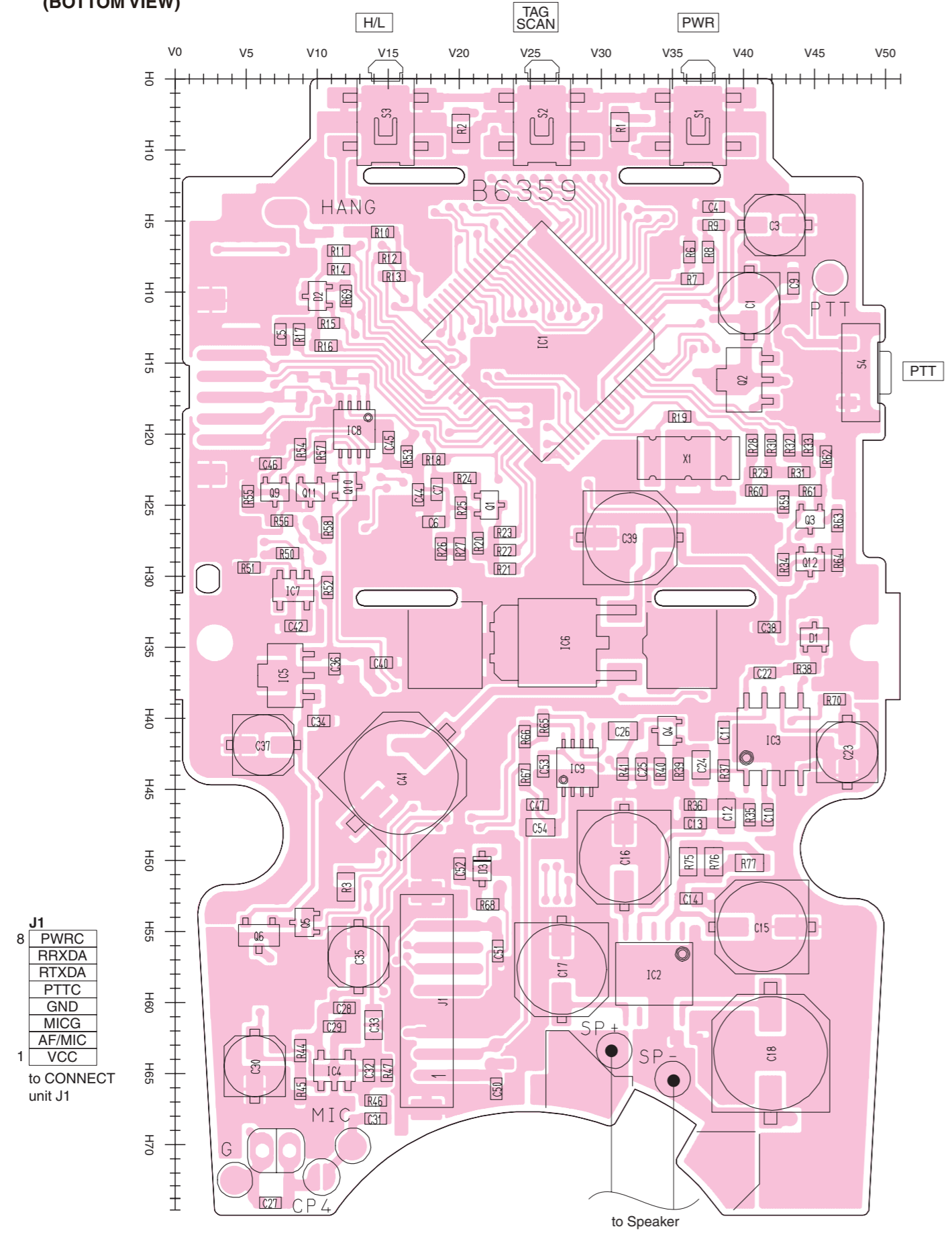
The combination of this page and the next page shows the unit layout in the same configuration as the actual P.C. Board.

9-9 HM-157 (Optional product) (TOP VIEW)

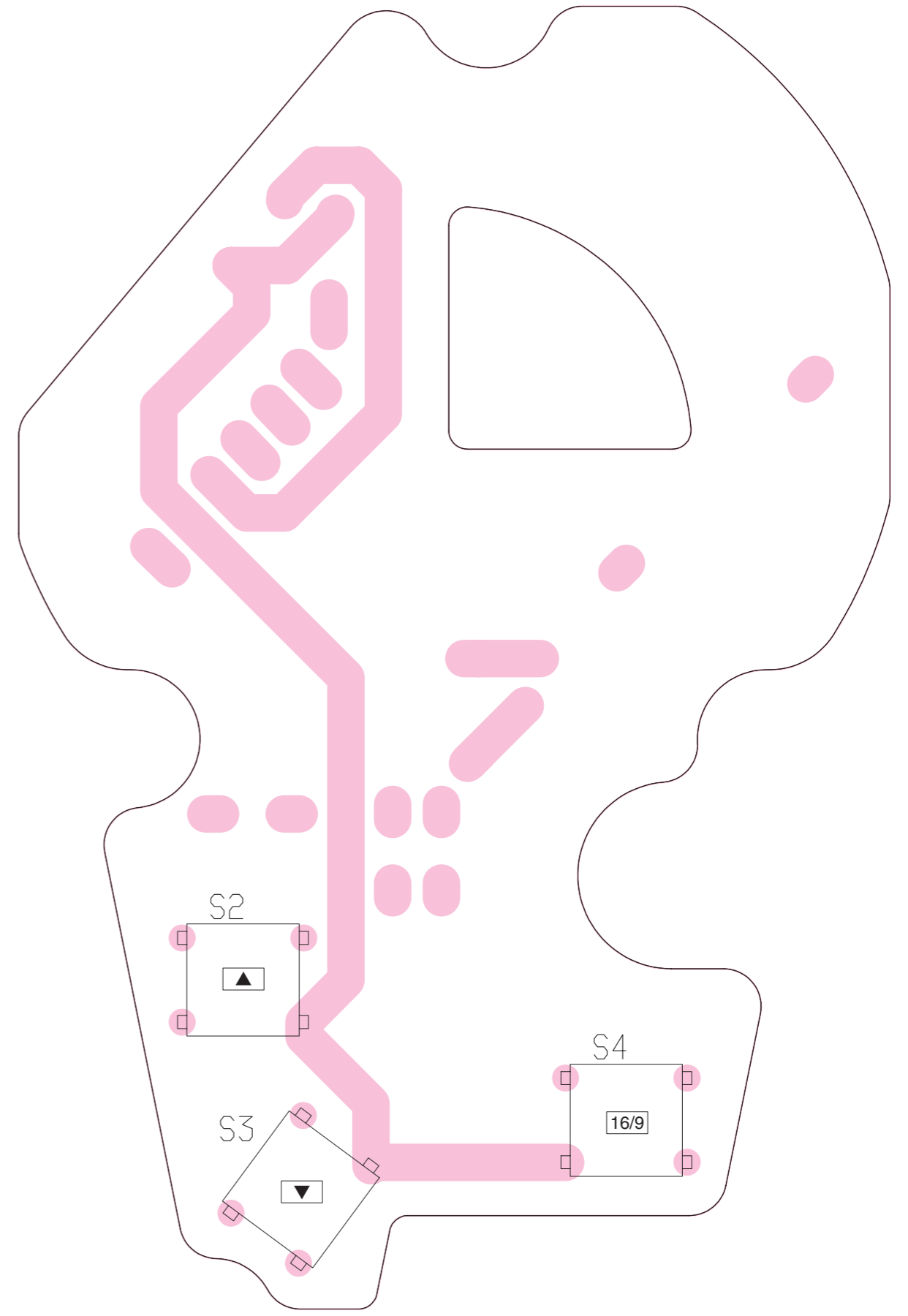


The combination of this page and the previous page shows the unit layout in the same configuration as the actual P.C. Board.

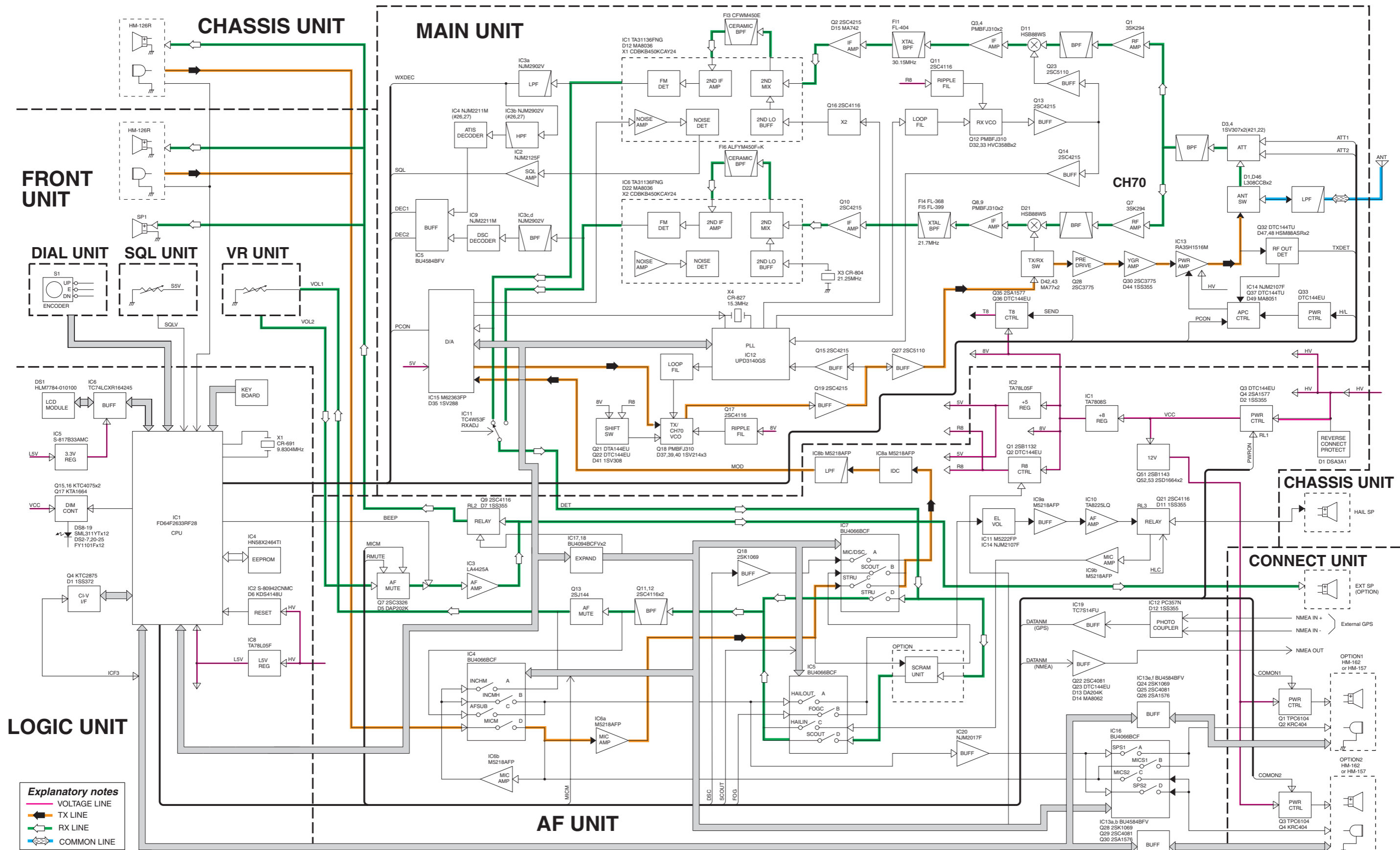
• HM-157 (Optional product)
(BOTTOM VIEW)



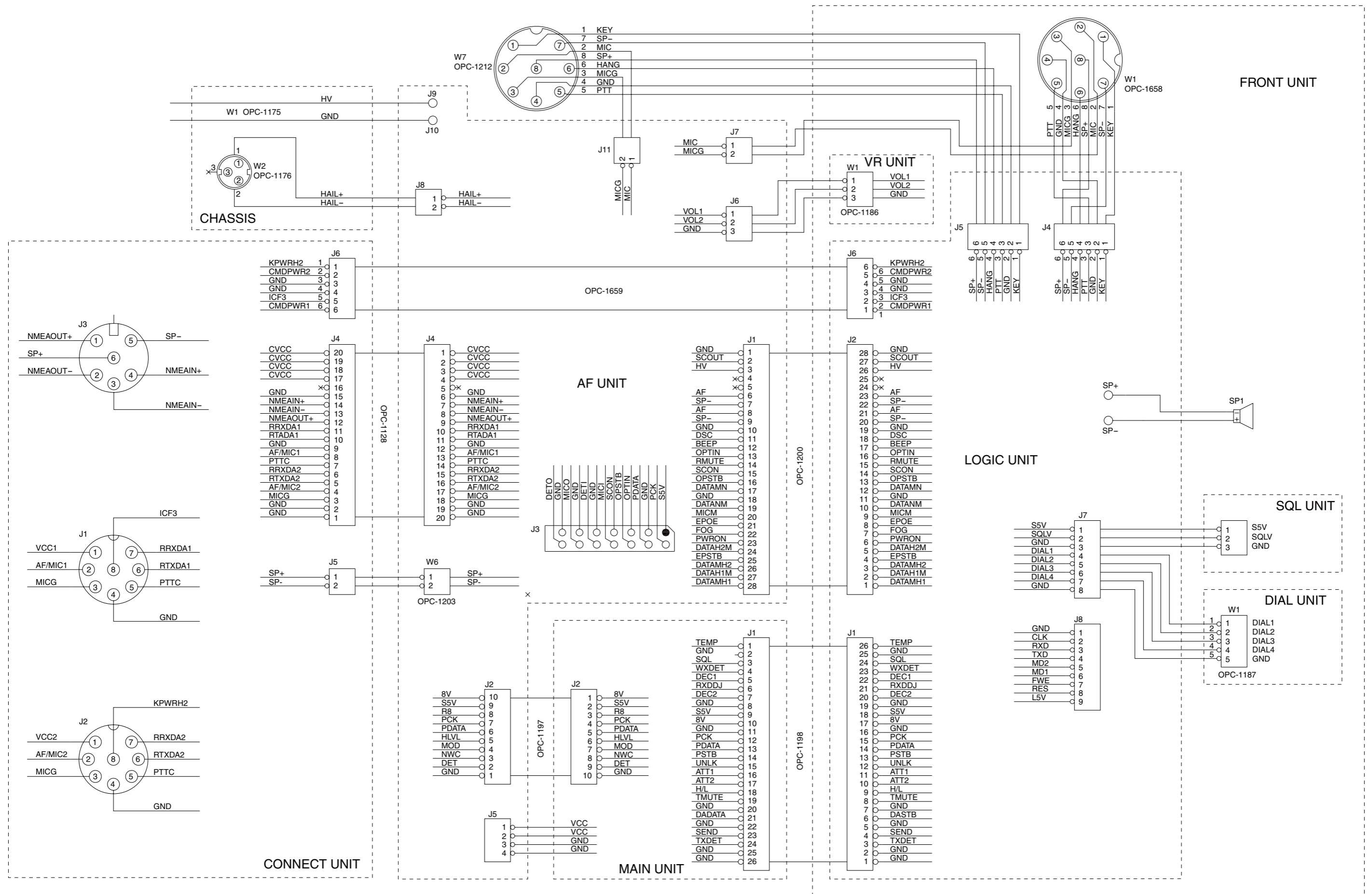
• HM-126RB-RG (BOTTOM VIEW)



SECTION 10 BLOCK DIAGRAM

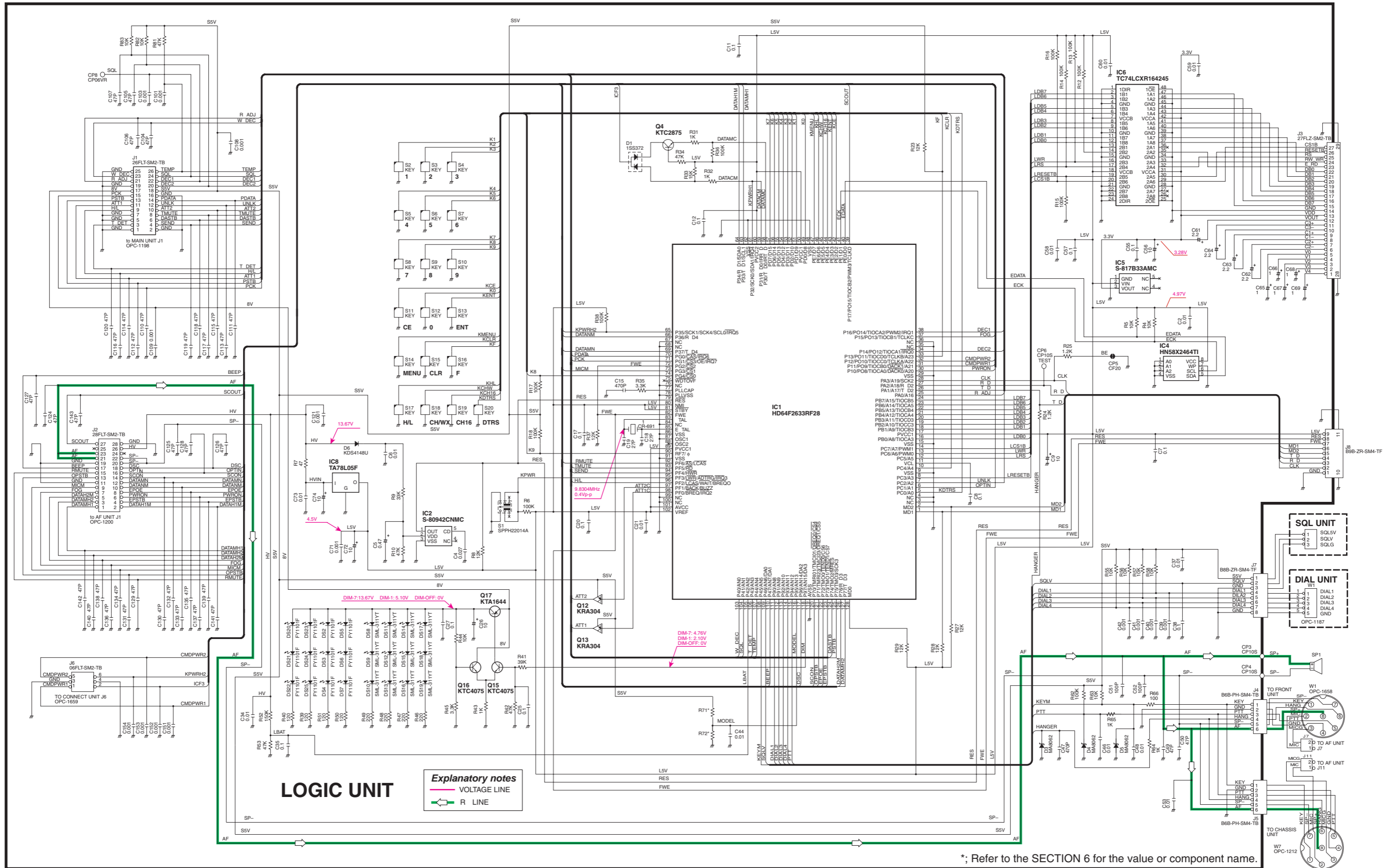


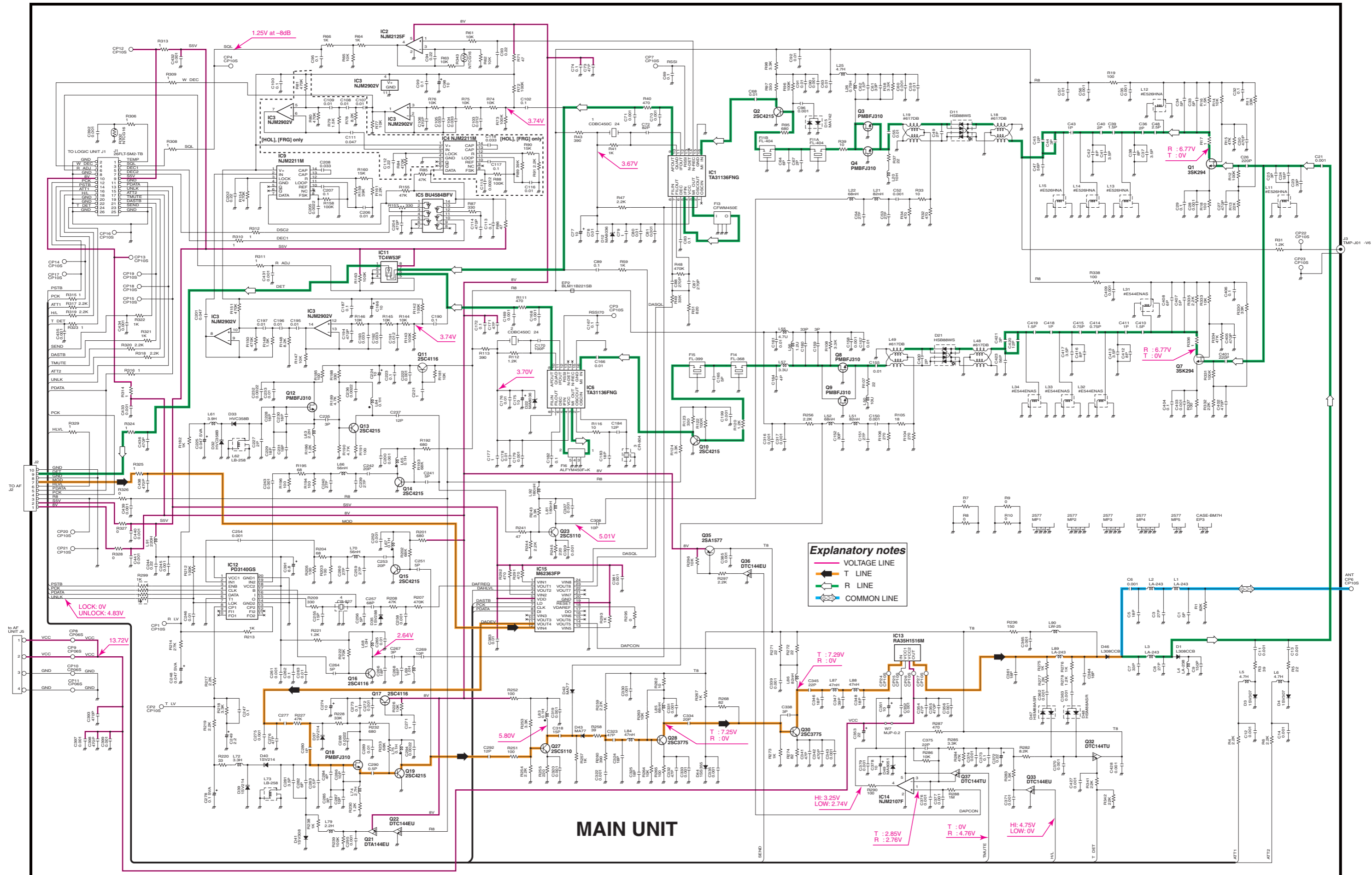
SECTION 11 WIRING DIAGRAM

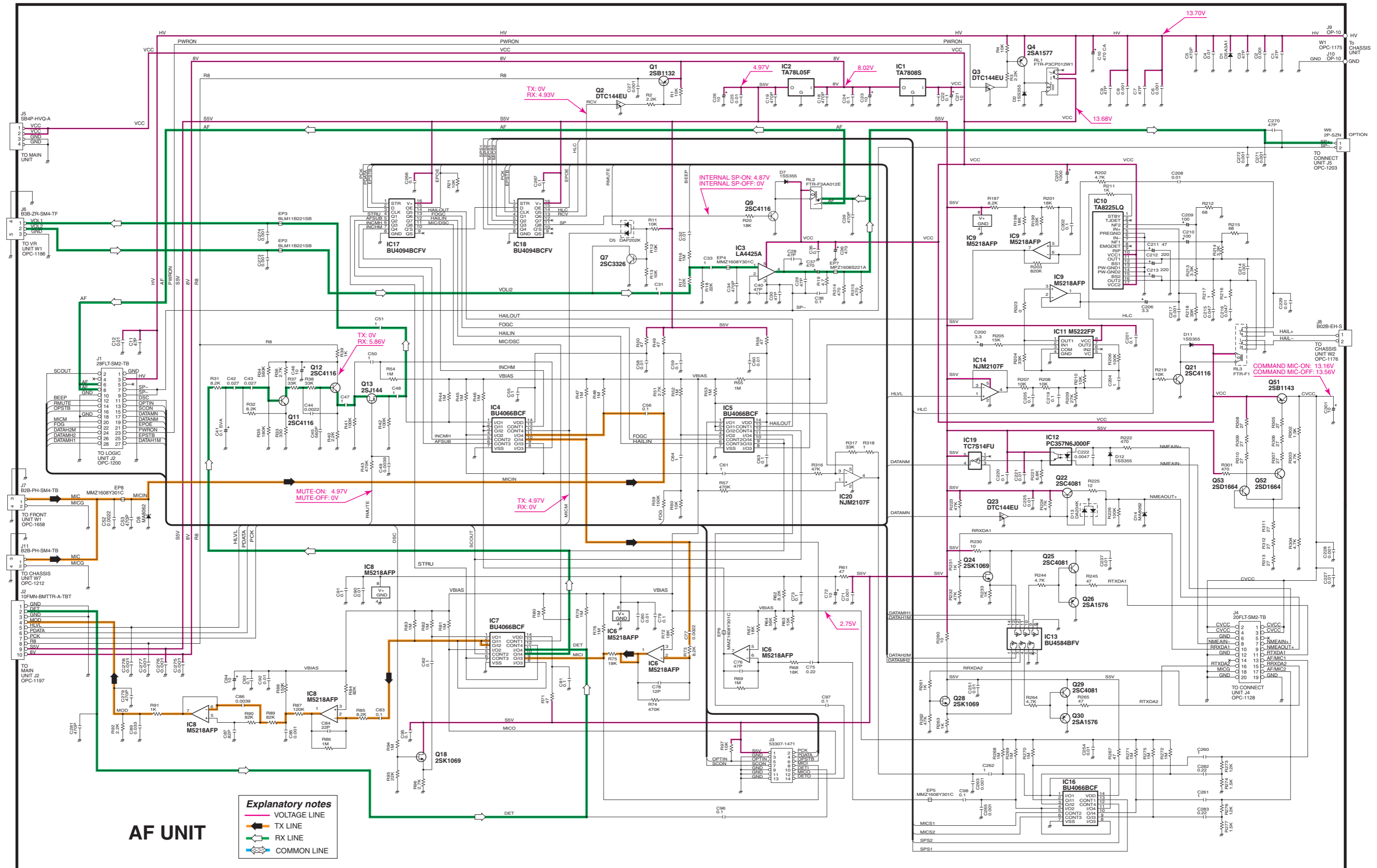


SECTION 12 VOLTAGE DIAGRAMS

12-1 LOGIC UNIT



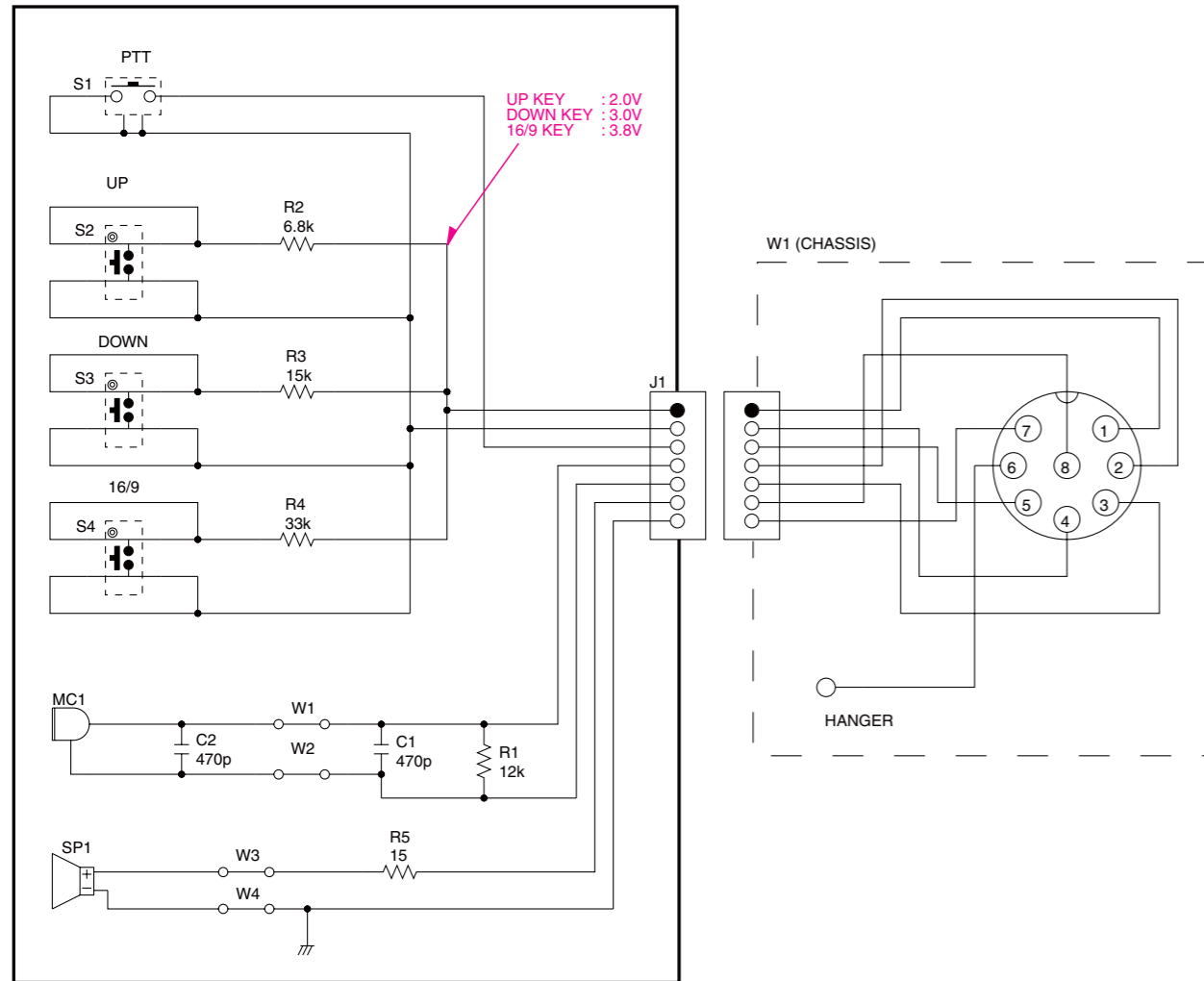




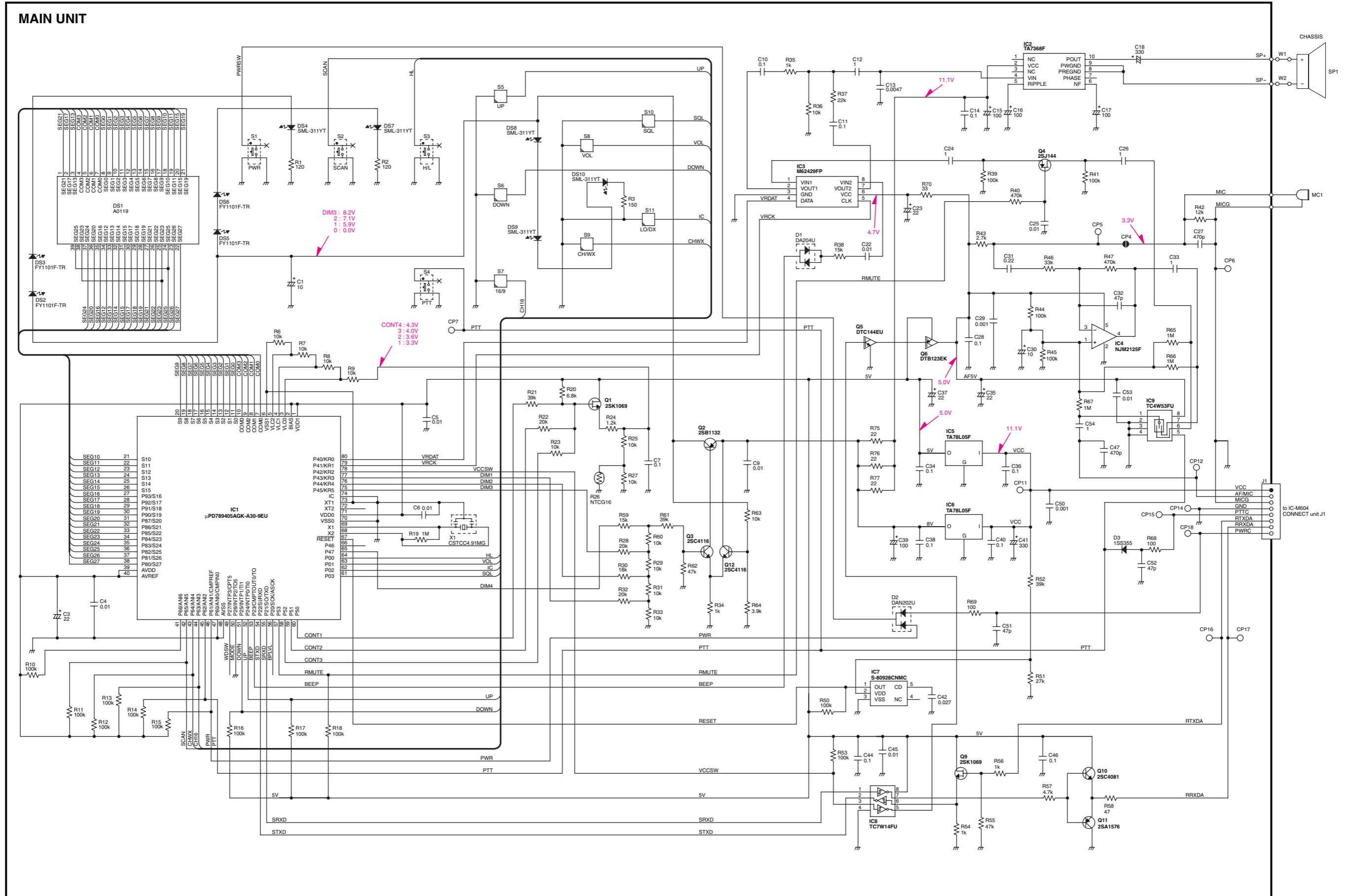
Explanatory notes
 — VOLTAGE LINE
 — TX LINE
 — RX LINE
 — COMMON LINE

AF UNIT

12-4 HM-126RB/RG



12-5 HM-157 (Optional product)



Icom Inc.

1-1-32, Kamiminami, Hirano-ku, Osaka 547-0003, Japan
Phone : +81 (06) 6793 5302
Fax : +81 (06) 6793 0013
URL : <http://www.icom.co.jp/world/index.html>

Icom America Inc.

<Corporate Headquarters>
2380 116th Avenue N.E., Bellevue, WA 98004, U.S.A.
Phone : +1 (425) 454-8155 Fax : +1 (425) 454-1509
URL : <http://www.icomamerica.com>
E-mail : sales@icomamerica.com
<Customer Service>
Phone : +1 (425) 454-7619

Icom Canada

Glenwood Centre #150-6165
Highway 17 Delta, B.C., V4K 5B8, Canada
Phone : +1 (604) 952-4266 Fax : +1 (604) 952-0090
URL : <http://www.icomcanada.com>
E-mail : info@icomcanada.com

Icom (Australia) Pty. Ltd.

Unit 1 / 103 Garden Road, Clayton VIC 3168 Australia
Phone : +61 (03) 9549-7500 Fax : +61 (03) 9549-7505
URL : <http://www.icom.net.au>
E-mail : sales@icom.net.au

Icom New Zealand

146A Harris Road, East Tamaki,
Auckland, New Zealand
Phone : +64 (09) 274 4062 Fax : +64 (09) 274 4708
URL : <http://www.icom.co.nz>
E-mail : inquiries@icom.co.nz

Beijing Icom Ltd.

Room C01, 10th Floor, Long Silver Mansion, No. 88,
Yong Ding Road, Haidian District, Beijing, 100039, China
Phone : +86 (010) 5889 4250 Fax : +86 (010) 5889 4250
URL : <http://www.bjicom.com>
E-mail : bjicom@bjicom.com

Icom (Europe) GmbH

Communication Equipment
Himmelgeister Str. 100, D-40225 Düsseldorf, Germany
Phone : +49 (0211) 346047 Fax : +49 (0211) 333639
URL : <http://www.icomeurope.com>
E-mail : info@icomeurope.com

Icom Spain S.L

Ctra. Rubi, 88, 08190, Sant Cugat del Valles, Barcelona, SPAIN
Phone : +34 (93) 590 26 70 Fax : +34 (93) 589 04 46
URL : <http://www.icomspain.com>
E-mail : icom@icomspain.com

Icom (UK) Ltd.

Unit 9, Sea St., Herne Bay, Kent, CT6 8LD, U.K.
Phone : +44 (01227) 741741 Fax : +44 (01227) 741742
URL : <http://www.icomuk.co.uk>
E-mail : info@icomuk.co.uk

Icom France s.a.s.

Zac de la Plaine
1 Rue Brindejonc des Moulinais, BP 45804,
31505 Toulouse Cedex 5, France
Phone : +33 (5) 61 36 03 03 Fax : +33 (5) 61 36 03 00
URL : <http://www.icom-france.com>
E-mail : icom@icom-france.com

Asia Icom Inc.

6F No.68, Sec. 1 Cheng-Teh Road, Taipei, Taiwan, R.O.C.
Phone : +886 (02) 2559 1899 Fax : +886 (02) 2559 1874
URL : <http://www.asia-icom.com>
E-mail : sales@asia-icom.com

Icom Polska

Sopot, 3 Maja 54 Poland
Phone : +48 (58) 550 7135 Fax : +48 (58) 551 0484
E-mail : icompolaska@icompolaska.com.pl

Count on us!